



LC420EUD

Product Specification

## SPECIFICATION FOR APPROVAL

( ) Preliminary Specification

(●) Final Specification

|       |                     |
|-------|---------------------|
| Title | 42.0" WUXGA TFT LCD |
|-------|---------------------|

|       |         |
|-------|---------|
| BUYER | Toshiba |
| MODEL |         |

|          |                      |
|----------|----------------------|
| SUPPLIER | LG Display Co., Ltd. |
| *MODEL   | LC420EUD             |
| SUFFIX   | SCA2(RoHS Verified)  |

\*When you obtain standard approval,  
please use the above model name without suffix

| APPROVED BY | SIGNATURE | DATE |
|-------------|-----------|------|
| /           |           |      |
| /           |           |      |
| /           |           |      |

Please return 1 copy for your confirmation with  
your signature and comments.

|                                                     |                                                                                       |          |
|-----------------------------------------------------|---------------------------------------------------------------------------------------|----------|
| APPROVED BY                                         | SIGNATURE                                                                             | DATE     |
| P.Y.Kim / Team Leader                               |  |          |
| REVIEWED BY                                         |                                                                                       |          |
| Y.S.Kim / Project Leader                            |  | 10.03.19 |
| PREPARED BY                                         |                                                                                       |          |
| S.H. Lee / Engineer                                 |  |          |
| TV Product Development Dept.<br>LG Display Co., Ltd |                                                                                       |          |

Ver. 1.0

1 / 40



LC420EUD

## Product Specification

**CONTENTS**

| Number | ITEM                                     | Page |
|--------|------------------------------------------|------|
|        | COVER                                    | 1    |
|        | CONTENTS                                 | 2    |
|        | RECORD OF REVISIONS                      | 3    |
| 1      | GENERAL DESCRIPTION                      | 4    |
| 2      | ABSOLUTE MAXIMUM RATINGS                 | 5    |
| 3      | ELECTRICAL SPECIFICATIONS                | 6    |
| 3-1    | ELECTRICAL CHARACTERISTICS               | 6    |
| 3-2    | INTERFACE CONNECTIONS                    | 8    |
| 3-3    | SIGNAL TIMING SPECIFICATIONS             | 11   |
| 3-4    | LVDS SIGNAL SPECIFICATIONS               | 12   |
| 3-5    | COLOR DATA REFERENCE                     | 15   |
| 3-6    | POWER SEQUENCE                           | 16   |
| 4      | OPTICAL SPECIFICATIONS                   | 18   |
| 5      | MECHANICAL CHARACTERISTICS               | 22   |
| 6      | RELIABILITY                              | 25   |
| 7      | INTERNATIONAL STANDARDS                  | 26   |
| 7-1    | SAFETY                                   | 26   |
| 7-2    | EMC                                      | 26   |
| 7-3    | Environment                              | 26   |
| 8      | PACKING                                  | 27   |
| 8-1    | INFORMATION OF LCM LABEL                 | 27   |
| 8-2    | PACKING FORM                             | 27   |
| 9      | PRECAUTIONS                              | 28   |
| 9-1    | MOUNTING PRECAUTIONS                     | 28   |
| 9-2    | OPERATING PRECAUTIONS                    | 28   |
| 9-3    | ELECTROSTATIC DISCHARGE CONTROL          | 29   |
| 9-4    | PRECAUTIONS FOR STRONG LIGHT EXPOSURE    | 29   |
| 9-5    | STORAGE                                  | 29   |
| 9-6    | HANDLING PRECAUTIONS FOR PROTECTION FILM | 29   |



LC420EUD

## Product Specification

**RECORD OF REVISIONS**

| Revision No. | Revision Date | Page  | Description                            |
|--------------|---------------|-------|----------------------------------------|
| 0.0          | Sep, 30, 2009 | -     | Preliminary Specification(First Draft) |
| 0.1          | Jan, 07, 2010 | 4     | General Description                    |
|              |               | 7     | Electric Characteristic                |
|              |               | 10    | LED Driver connection configuration    |
|              |               | 22    | Mechanical Characteristics             |
|              |               | 23    | Front View                             |
|              |               | 24    | Rear View                              |
| 0.2          | Jan, 07, 2010 | 27    | Pallet Size                            |
|              |               | 31    | Serial NO. delete                      |
| 0.3          | Jan, 23, 2010 | 4     | General Features                       |
|              |               | 22    | Mechanical Characteristics             |
| 0.4          | Feb, 10, 2010 | 6     | Electrical specifications              |
|              |               | 7     | Electrical Characteristics             |
|              |               | 18    | Optical specification                  |
|              |               | 19    | Luminance                              |
|              | Feb, 11, 2010 | 4     | General Description                    |
|              |               | 7     | Electric Characteristic                |
|              |               | 10    | LED Driver connector pin configuration |
| 0.5          | Mar, 12, 2010 | 4     | Surface Treatment                      |
| 1.0          | Mar, 16, 2010 | 8,9   | LCD Connector                          |
|              |               | 18    | Contrast Ratio                         |
|              |               | 39    | Gray to Gray Response Time             |
|              |               | 40    | MPRT Response Time                     |
|              |               | 27    | Factory code                           |
|              |               | 19    | Gray scale                             |
|              |               | 8     | Pin configuration                      |
|              |               | 9     | Brightness Adjust                      |
|              | Mar, 18, 2010 | 40,41 | Local dimming interface design Guide   |
|              |               | -     | Final Specification                    |
|              |               |       |                                        |
|              |               |       |                                        |



LC420EUD

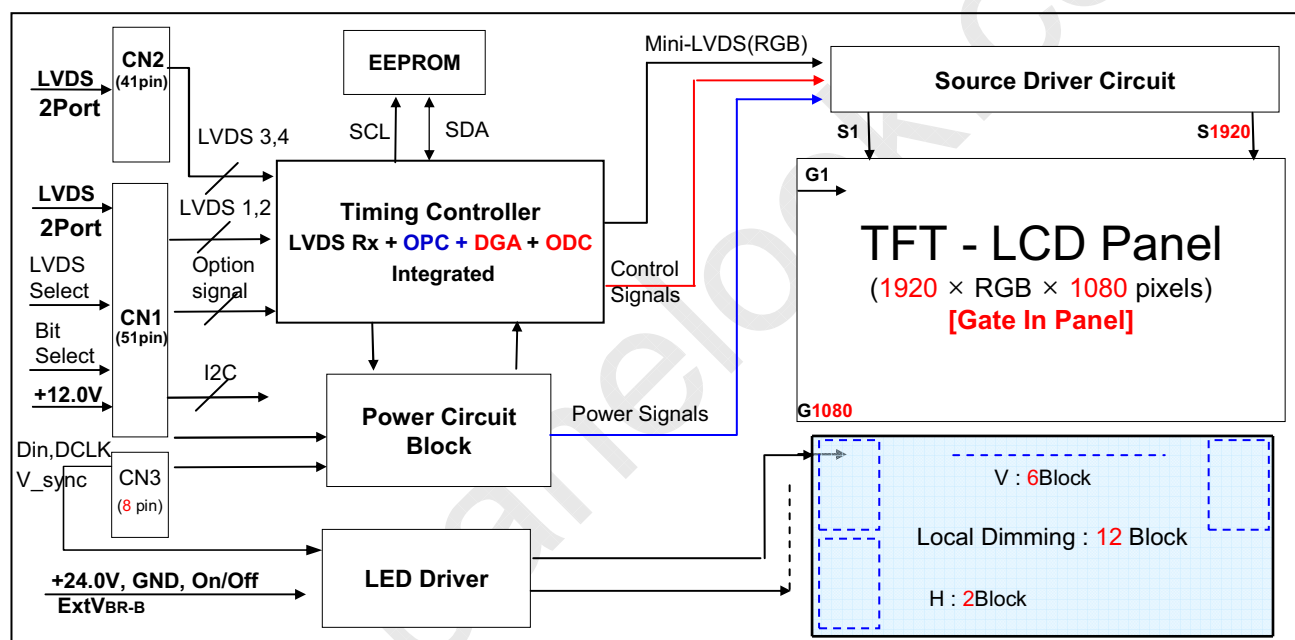
## Product Specification

## 1. General Description

The **LC420EUD** is a Color Active Matrix Liquid Crystal Display with an integral **Light Emitting Diode (LED)** backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a **42.02 inch** diagonally measured active display area with **WUXGA** resolution (**1080** vertical by **1920** horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a **10-bit** gray scale signal for each dot. Therefore, it can present a palette of more than **1.06Billion** colors.

It has been designed to apply the **10-bit 4-port LVDS** interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



## General Features

|                       |                                                                                       |
|-----------------------|---------------------------------------------------------------------------------------|
| Active Screen Size    | 42.02 inches(1067.31mm) diagonal                                                      |
| Outline Dimension     | <b>973.2(H) × 566.2(V) X 10.8(B)/23.6 mm(D) (Typ.)</b>                                |
| Pixel Pitch           | 0.4845 mm x 0.4845 mm                                                                 |
| Pixel Format          | 1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement                              |
| Color Depth           | 10bit(D), 1.06Billion colors                                                          |
| Luminance, White      | 450 cd/m <sup>2</sup> (Center 1point ,Typ.)                                           |
| Viewing Angle (CR>10) | Viewing angle free ( R/L 178 (Min.), U/D 178 (Min.))                                  |
| Power Consumption     | Total 98W (Typ.) [Logic= 10W, LED Driver= 88W (ExtVbr_B=100% )]                       |
| Weight                | <b>11.3 Kg (Typ.) , 12.0Kg(Max)</b>                                                   |
| Display Mode          | Transmissive mode, Normally black                                                     |
| Surface Treatment     | Hard coating(3H), Anti-reflection treatment of the front polarizer (Reflectance < 2%) |

LC420EUD

## Product Specification

## 2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

**Table 1. ABSOLUTE MAXIMUM RATINGS**

| Parameter                      |             | Symbol               | Value |        | Unit | Note |
|--------------------------------|-------------|----------------------|-------|--------|------|------|
|                                |             |                      | Min   | Max    |      |      |
| Power Input Voltage            | LCD Circuit | VLCD                 | -0.3  | +14.0  | VDC  | 1    |
|                                | Driver      | VBL                  | -0.3  | + 27.0 | VDC  |      |
| Driver Control Voltage         | ON/OFF      | VOFF / VON           | -0.3  | +5.5   | VDC  |      |
|                                | Brightness  | EXTV <sub>BR-B</sub> | 0.0   | +5.5   | VDC  |      |
| T-Con Option Selection Voltage |             | VLOGIC               | -0.3  | +4.0   | VDC  |      |
| Operating Temperature          |             | T <sub>OP</sub>      | 0     | +50    | °C   | 2,3  |
| Storage Temperature            |             | T <sub>ST</sub>      | -20   | +60    | °C   |      |
| Panel Front Temperature        |             | T <sub>SUR</sub>     | -     | +68    | °C   | 4    |
| Operating Ambient Humidity     |             | H <sub>OP</sub>      | 10    | 90     | %RH  | 2,3  |
| Storage Humidity               |             | H <sub>ST</sub>      | 10    | 90     | %RH  |      |

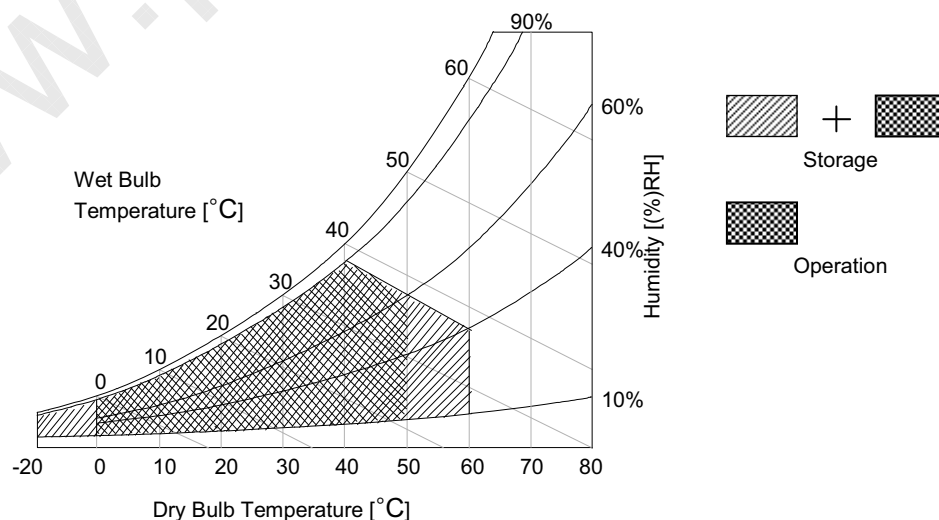
Note1. Ambient temperature condition ( $T_a = 25 \pm 2 \text{ }^\circ\text{C}$ )

2. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be Max 39°C, and no condensation of water.

3. Gravity mura can be guaranteed below 40°C condition.

4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may degraded in case of improper thermal management in final product design.





LC420EUD

## Product Specification

### 3. Electrical Specifications

#### 3-1. Electrical Characteristics

It requires two power inputs. One is employed to power for the LCD circuit. The other is used for the LED backlight and LED Driver circuit.

**Table 2. ELECTRICAL CHARACTERISTICS**

| Parameter           | Symbol            | Value |      |       | Unit            | Note |
|---------------------|-------------------|-------|------|-------|-----------------|------|
|                     |                   | Min   | Typ  | Max   |                 |      |
| Circuit :           |                   |       |      |       |                 |      |
| Power Input Voltage | V <sub>LCD</sub>  | 10.8  | 12.0 | 13.2  | V <sub>DC</sub> |      |
| Power Input Current | I <sub>LCD</sub>  | -     | 815  | 1060  | mA              | 1    |
|                     |                   | -     | 1140 | 1482  | mA              | 2    |
| Power Consumption   | P <sub>LCD</sub>  |       | 9.78 | 12.72 | Watt            | 1    |
| Rush current        | I <sub>RUSH</sub> | -     | -    | 5     | A               | 3    |

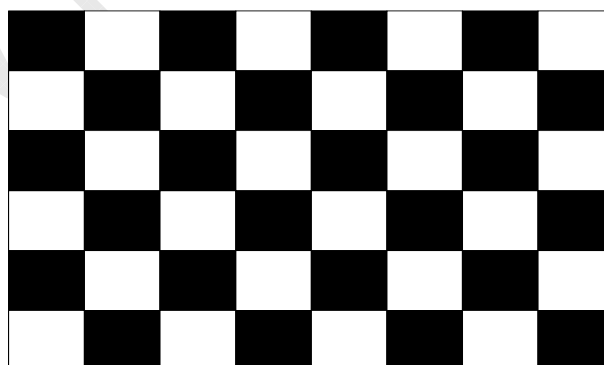
Note 1. The specified current and power consumption are under the  $V_{LCD}=12.0V$ ,  $T_a=25 \pm 2^{\circ}C$ ,  $f_v=120Hz$  condition whereas mosaic pattern(8 x 6) is displayed and  $f_v$  is the frame frequency.

2. The current is specified at the maximum current pattern.

3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).

White : 1023 Gray

Black : 0 Gray

**Mosaic Pattern(8 x 6)**

LC420EUD

## Product Specification

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

| Parameter                                |                              |     | Symbol   | Values |        |      | Unit | Notes                                |
|------------------------------------------|------------------------------|-----|----------|--------|--------|------|------|--------------------------------------|
|                                          |                              |     |          | Min    | Typ    | Max  |      |                                      |
| LED Driver :                             |                              |     |          |        |        |      |      |                                      |
| Power Supply Input Voltage               |                              |     | VBL      | 22.8   | 24.0   | 25.2 | Vdc  | 1                                    |
| Power Supply Input Current               |                              |     | IBL_A    | -      | 3.7    | 4.0  | A    | Ext VBR-B = 100%                     |
| Power Supply Input Current (In-Rush)     |                              |     | Inrush   | -      | -      | 6.5  | A    | VBL = 22.8V<br>Ext VBR-B = 100%<br>4 |
| Power Consumption                        |                              |     | PBL      | -      | 88     | 94.1 | W    | Ext VBR-B = 100%                     |
| Input Voltage for Control System Signals | On/Off                       | On  | V on     | 2.5    | -      | 5.0  | Vdc  |                                      |
|                                          |                              | Off | V off    | -0.3   | 0.0    | 0.7  | Vdc  |                                      |
|                                          | Brightness Adjust            |     | ExtVBR-B | 0      | -      | 100  | %    | On Duty<br>6                         |
|                                          | PWM Frequency for NTSC & PAL |     | PAL      | 97     | 100    | 103  | Hz   | 3                                    |
|                                          |                              |     | NTSC     | 117    | 120    | 123  | Hz   | 3                                    |
| LED :                                    |                              |     |          |        |        |      |      |                                      |
| Life Time                                |                              |     |          | 30,000 | 50,000 |      | Hrs  | 2                                    |

## Notes :

1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes at  $25\pm 2^{\circ}\text{C}$ . The specified current and power consumption are under the typical supply Input voltage 24V and VBR (ExtVBR-B : 100%), it is total power consumption.
2. The life time (MTTF) is determined as the time which luminance of the LED is 50% compared to that of initial value at the typical LED current (ExtVBR-B : 100%) on condition of continuous operating in LCM state at  $25\pm 2^{\circ}\text{C}$ .
3. LGD recommend that the PWM freq. is synchronized with One time harmonic of Vsync signal of system. Though PWM frequency is over 120Hz (max 252Hz), function of LED Driver is not affected.
4. The duration of rush current is about 10ms.
5. Even though inrush current is over the specified value, there is no problem if I<sup>2</sup>T spec of fuse is satisfied.
6. Ext\_PWM Signal have to input available duty range.







LC420EUD

## Product Specification

## 3-2. Interface Connections

This LCD module employs two kinds of interface connection, 51-pin connector and 41-pin connector are used for the module electronics and 14-pin connector is used for the integral backlight system.

## 3-2-1. LCD Module

- LCD Connector : FI-R51S-HF(manufactured by JAE)  
(CN1) Refer to below and next Page table
- Mating Connector : FI-R51HL(JAE) or compatible

Table 4-1. MODULE CONNECTOR(CN1) PIN CONFIGURATION

| No | Symbol       | Description                         | No | Symbol     | Description                          |
|----|--------------|-------------------------------------|----|------------|--------------------------------------|
| 1  | NC           | No Connection                       | 27 | Bit Select | 'H' or NC= 10bit(D) , 'L' = 8bit     |
| 2  | NC           | No Connection                       | 28 | R2AN       | SECOND LVDS Receiver Signal (A-)     |
| 3  | NC           | No Connection                       | 29 | R2AP       | SECOND LVDS Receiver Signal (A+)     |
| 4  | SDA          | I2C Data Line                       | 30 | R2BN       | SECOND LVDS Receiver Signal (B-)     |
| 5  | SCL          | I2C Clock Line                      | 31 | R2BP       | SECOND LVDS Receiver Signal (B+)     |
| 6  | WP           | No Connection (Reserved for LGD)    | 32 | R2CN       | SECOND LVDS Receiver Signal (C-)     |
| 7  | LVDS Select  | 'H' =JEIDA , 'L' or NC = VESA       | 33 | R2CP       | SECOND LVDS Receiver Signal (C+)     |
| 8  | NC           | No Connection                       | 34 | GND        | Ground                               |
| 9  | NC           | No Connection                       | 35 | R2CLKN     | SECOND LVDS Receiver Clock Signal(-) |
| 10 | L-DIM Enable | 'H' = Enable , 'L' or NC = Disable  | 36 | R2CLKP     | SECOND LVDS Receiver Clock Signal(+) |
| 11 | GND          | Ground                              | 37 | GND        | Ground                               |
| 12 | R1AN         | FIRST LVDS Receiver Signal (A-)     | 38 | R2DN       | SECOND LVDS Receiver Signal (D-)     |
| 13 | R1AP         | FIRST LVDS Receiver Signal (A+)     | 39 | R2DP       | SECOND LVDS Receiver Signal (D+)     |
| 14 | R1BN         | FIRST LVDS Receiver Signal (B-)     | 40 | R2EN       | SECOND LVDS Receiver Signal (E-)     |
| 15 | R1BP         | FIRST LVDS Receiver Signal (B+)     | 41 | R2EP       | SECOND LVDS Receiver Signal (E+)     |
| 16 | R1CN         | FIRST LVDS Receiver Signal (C-)     | 42 | NC         | No Connection                        |
| 17 | R1CP         | FIRST LVDS Receiver Signal (C+)     | 43 | NC         | No Connection                        |
| 18 | GND          | Ground                              | 44 | GND        | Ground                               |
| 19 | R1CLKN       | FIRST LVDS Receiver Clock Signal(-) | 45 | GND        | Ground                               |
| 20 | R1CLKP       | FIRST LVDS Receiver Clock Signal(+) | 46 | GND        | Ground                               |
| 21 | GND          | Ground                              | 47 | NC         | No connection                        |
| 22 | R1DN         | FIRST LVDS Receiver Signal (D-)     | 48 | VLCD       | Power Supply +12.0V                  |
| 23 | R1DP         | FIRST LVDS Receiver Signal (D+)     | 49 | VLCD       | Power Supply +12.0V                  |
| 24 | R1EN         | FIRST LVDS Receiver Signal (E-)     | 50 | VLCD       | Power Supply +12.0V                  |
| 25 | R1EP         | FIRST LVDS Receiver Signal (E+)     | 51 | VLCD       | Power Supply +12.0V                  |
| 26 | NC           | No Connection                       | -  | -          | -                                    |

- Note
1. All GND(ground) pins should be connected together to the LCD module's metal frame.
  2. All VLCD (power input) pins should be connected together.
  3. All Input levels of LVDS signals are based on the EIA 644 Standard.
  4. Specific pins(pin No. #2~#6) are used for internal data process of the LCD module.  
These pins should be no connection.
  5. Specific pins(pin No. #10) are used for Local Dimming function of the LCD module.  
If not used, these pins are no connection. (Please see the Appendix III-4 for more information.)
  6. LVDS pin (pin No. #24,25,40,41) are used for 10Bit(D) of the LCD module.  
If used for 8Bit(R), these pins are no connection.
  7. Specific pin No. #44 is used for "No signal detection" of system signal interface.  
It should be GND for NSB(No Signal Black) during the system interface signal is not.  
If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).





LC420EUD

## Product Specification

-LCD Connector : FI-RE41S-HF (manufactured by JAE)

(CN2)

- Mating Connector : FI-RE41HL

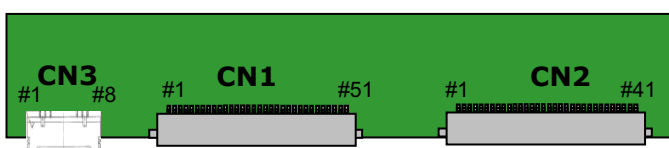
**Table 4-2. MODULE CONNECTOR(CN2) PIN CONFIGURATION**

| No | Symbol | Description                         | No | Symbol | Description                         |
|----|--------|-------------------------------------|----|--------|-------------------------------------|
| 1  | NC     | No connection(Reserved)             | 22 | RE3N   | THIRD LVDS Receiver Signal (E-)     |
| 2  | NC     | No connection                       | 23 | RE3P   | THIRD LVDS Receiver Signal (E+)     |
| 3  | NC     | No connection                       | 24 | GND    | Ground                              |
| 4  | NC     | No connection                       | 25 | GND    | Ground                              |
| 5  | NC     | No connection                       | 26 | RA4N   | FORTH LVDS Receiver Signal (A-)     |
| 6  | NC     | No connection                       | 27 | RA4P   | FORTH LVDS Receiver Signal (A+)     |
| 7  | NC     | No connection                       | 28 | RB4N   | FORTH LVDS Receiver Signal (B-)     |
| 8  | NC     | No connection                       | 29 | RB4P   | FORTH LVDS Receiver Signal (B+)     |
| 9  | GND    | Ground                              | 30 | RC4N   | FORTH LVDS Receiver Signal (C-)     |
| 10 | RA3N   | THIRD LVDS Receiver Signal (A-)     | 31 | RC4P   | FORTH LVDS Receiver Signal (C+)     |
| 11 | RA3P   | THIRD LVDS Receiver Signal (A+)     | 32 | GND    | Ground                              |
| 12 | RB3N   | THIRD LVDS Receiver Signal (B-)     | 33 | RCLK4N | FORTH LVDS Receiver Clock Signal(-) |
| 13 | RB3P   | THIRD LVDS Receiver Signal (B+)     | 34 | RCLK4P | FORTH LVDS Receiver Clock Signal(+) |
| 14 | RC3N   | THIRD LVDS Receiver Signal (C-)     | 35 | GND    | Ground                              |
| 15 | RC3P   | THIRD LVDS Receiver Signal (C+)     | 36 | RD4N   | FORTH LVDS Receiver Signal (D-)     |
| 16 | GND    | Ground                              | 37 | RD4P   | FORTH LVDS Receiver Signal (D+)     |
| 17 | RCLK3N | THIRD LVDS Receiver Clock Signal(-) | 38 | RE4N   | FORTH LVDS Receiver Signal (E-)     |
| 18 | RCLK3P | THIRD LVDS Receiver Clock Signal(+) | 39 | RE4P   | FORTH LVDS Receiver Signal (E+)     |
| 19 | GND    | Ground                              | 40 | GND    | Ground                              |
| 20 | RD3N   | THIRD LVDS Receiver Signal (D-)     | 41 | GND    | Ground                              |
| 21 | RD3P   | THIRD LVDS Receiver Signal (D+)     | -  |        |                                     |

Note : 1. All GND(ground) pins should be connected together to the LCD module's metal frame.  
2. LVDS pin (**pin No. #22,23,38,39**) are used for 10Bit(D) of the LCD module.  
If used for 8Bit(R), these pins are no connection.

**Table 4-3. MODULE CONNECTOR(CN3) PIN CONFIGURATION**

| No | Symbol   | Description                |
|----|----------|----------------------------|
| 1  | VSYNC    | Vertical Sync signal       |
| 2  | GND      | Backlight Ground           |
| 3  | GND      | Backlight Ground           |
| 4  | SIN      | Local Dimming Serial Data  |
| 5  | GND      | Backlight Ground           |
| 6  | SCLK     | Local Dim Serial Clock     |
| 7  | Reserved | No Connection              |
| 8  | Reverse  | Local Dimming data Reverse |

**Rear view of LCM**

Ver. 1.0

9 /42

LC420EUD

## Product Specification

## 3-2-2. Backlight Module

**Master**

- LED Driver Connector : 20022WR-14B1(Yeonho)  
or Equivalent
- Mating Connector : 20022HS-14 or Equivalent

Table 5. LED DRIVER CONNECTOR PIN CONFIGURATION

| Pin No | Symbol   | Description              | Note |
|--------|----------|--------------------------|------|
| 1      | VBL      | Power Supply +24.0V      |      |
| 2      | VBL      | Power Supply +24.0V      |      |
| 3      | VBL      | Power Supply +24.0V      |      |
| 4      | VBL      | Power Supply +24.0V      |      |
| 5      | VBL      | Power Supply +24.0V      |      |
| 6      | GND      | Backlight Ground         | 1    |
| 7      | GND      | Backlight Ground         |      |
| 8      | GND      | Backlight Ground         |      |
| 9      | GND      | Backlight Ground         |      |
| 10     | GND      | Backlight Ground         |      |
| 11     | NC       | No connection            |      |
| 12     | VON/OFF  | Backlight ON/OFF control |      |
| 13     | EXTVBR-B | External PWM             | 2    |
| 14     | Status   | Backlight Status         | 3    |

Notes : 1. GND should be connected to the LCD module's metal frame.

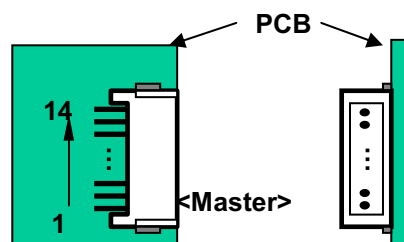
2. High : on duty / Low : off duty, Pin#13 can be opened. ( if Pin #13 is open , EXTVBR-B is 100% )

3. Normal : Low (Under 0.7V)

Abnormal : High(Over 2.5V)

4. Each impedance of pin #12 and 13 is over 50 [KΩ].

## ◆ Rear view of LCM





LC420EUD

## Product Specification

## 3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

Table 6-1. TIMING TABLE for NTSC (DE Only Mode)

| ITEM       |                | Symbol          | Min   | Typ   | Max   | Unit  | Note     |
|------------|----------------|-----------------|-------|-------|-------|-------|----------|
| Horizontal | Display Period | t <sub>HV</sub> | 480   | 480   | 480   | tCLK  | 1920 / 4 |
|            | Blank          | t <sub>HB</sub> | 40    | 70    | 200   | tCLK  | 1        |
|            | Total          | t <sub>HP</sub> | 520   | 550   | 680   | tCLK  |          |
| Vertical   | Display Period | t <sub>VV</sub> | 1080  | 1080  | 1080  | Lines |          |
|            | Blank          | t <sub>VB</sub> | 16    | 45    | 86    | Lines | 1        |
|            | Total          | t <sub>VP</sub> | 1096  | 1125  | 1166  | Lines |          |
| Frequency  | DCLK           | fCLK            | 66.97 | 74.25 | 78.00 | MHz   |          |
|            | Horizontal     | f <sub>H</sub>  | 121.8 | 135   | 140   | KHz   | 2        |
|            | Vertical       | f <sub>V</sub>  | 108   | 120   | 122   | Hz    | 2        |

Table 6-2 TIMING TABLE for DVB/PAL (DE Only Mode)

| ITEM       |                | Symbol          | Min   | Typ   | Max   | Unit  | Note     |
|------------|----------------|-----------------|-------|-------|-------|-------|----------|
| Horizontal | Display Period | t <sub>HV</sub> | 480   | 480   | 480   | tCLK  | 1920 / 4 |
|            | Blank          | t <sub>HB</sub> | 40    | 70    | 200   | tCLK  | 1        |
|            | Total          | t <sub>HP</sub> | 520   | 550   | 680   | tCLK  |          |
| Vertical   | Display Period | t <sub>VV</sub> | 1080  | 1080  | 1080  | Lines |          |
|            | Blank          | t <sub>VB</sub> | 228   | 270   | 300   | Lines | 1        |
|            | Total          | t <sub>VP</sub> | 1308  | 1350  | 1380  | Lines |          |
| Frequency  | DCLK           | fCLK            | 66.97 | 74.25 | 78.00 | MHz   |          |
|            | Horizontal     | f <sub>H</sub>  | 121.8 | 135   | 140   | KHz   | 2        |
|            | Vertical       | f <sub>V</sub>  | 95    | 100   | 104   | Hz    | 2        |

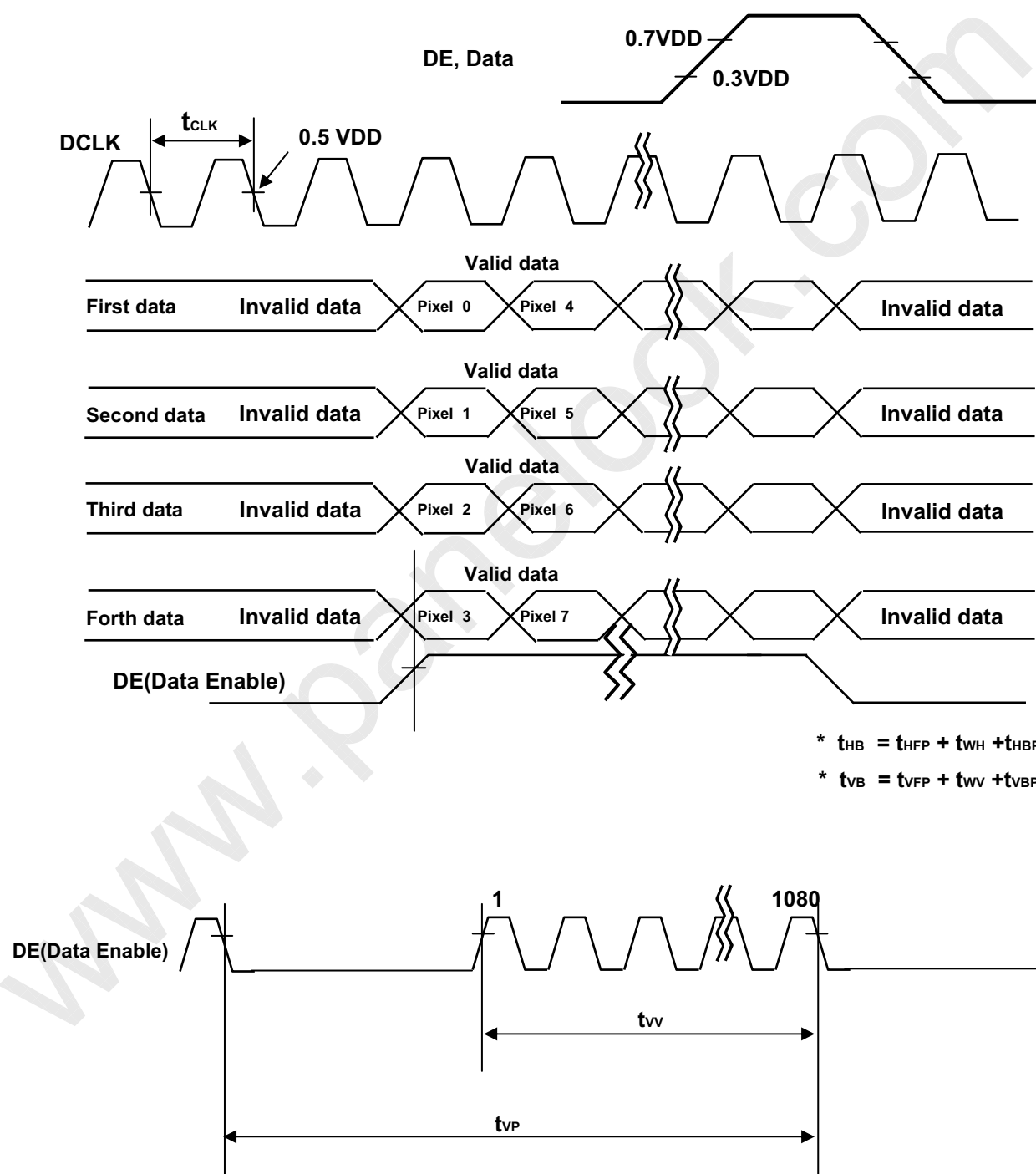
- Note
1. The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode).  
If you use spread spectrum for EMI, add some additional clock to minimum value for clock margin.
  2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency.

LC420EUD

## Product Specification

## 3-4. LVDS Signal Specification

## 3-4-1. LVDS Input Signal Timing Diagram

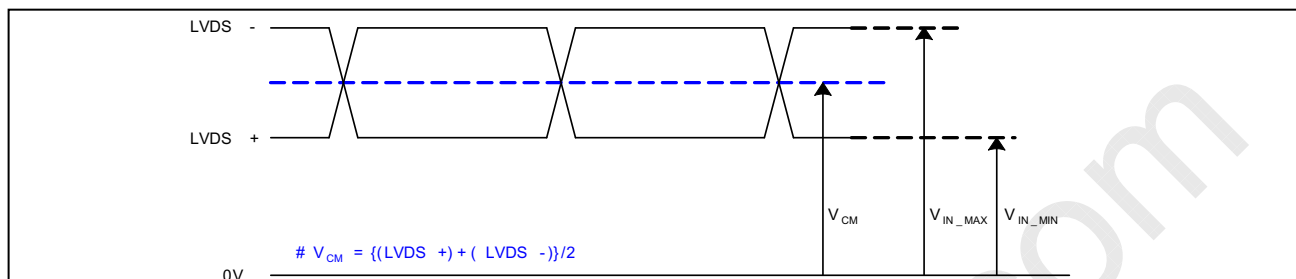


LC420EUD

## Product Specification

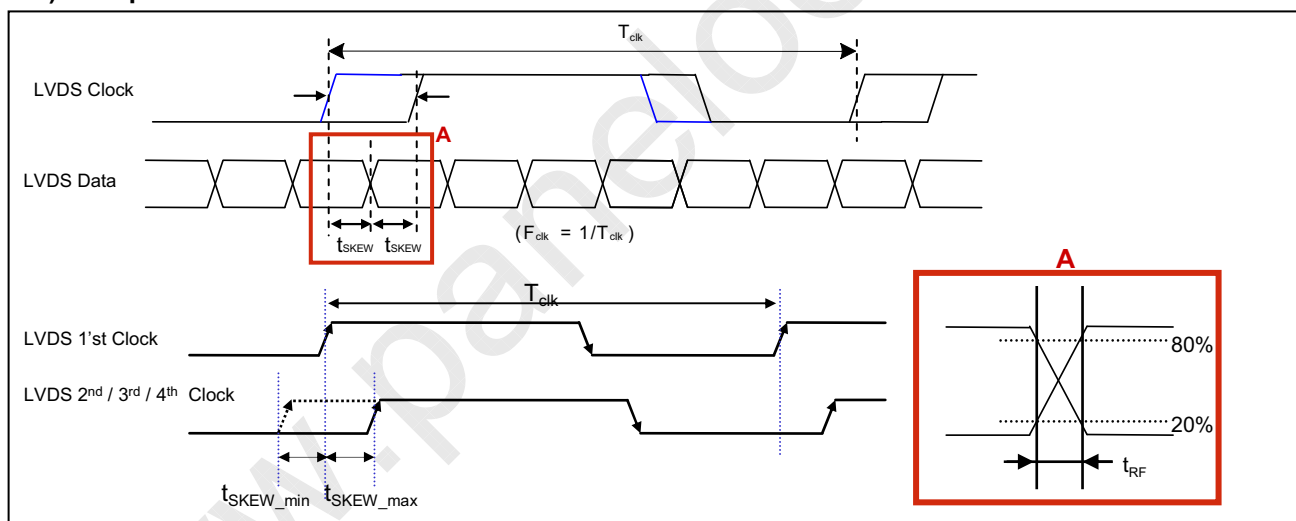
## 3-4-2. LVDS Input Signal Characteristics

## 1) DC Specification



| Description                   | Symbol          | Min | Max | Unit | Note |
|-------------------------------|-----------------|-----|-----|------|------|
| LVDS Common mode Voltage      | $V_{CM}$        | 1.0 | 1.5 | V    | -    |
| LVDS Input Voltage Range      | $V_{IN}$        | 0.7 | 1.8 | V    | -    |
| Change in common mode Voltage | $\Delta V_{CM}$ |     | 250 | mV   | -    |

## 2) AC Specification



| Description                                   |                | Symbol               | Min  | Max                          | Unit             | Note |
|-----------------------------------------------|----------------|----------------------|------|------------------------------|------------------|------|
| LVDS Differential Voltage                     | High Threshold | V <sub>TH</sub>      | 100  | 300                          | mV               | 3    |
|                                               | Low Threshold  | V <sub>TL</sub>      | -300 | -100                         | mV               |      |
| LVDS Clock to Data Skew Margin                |                | t <sub>SKEW</sub>    |      | (0.25*T <sub>clk</sub> ) / 7 | ps               | -    |
| LVDS Clock/DATA Rising/Falling time           |                | t <sub>RF</sub>      | 260  | (0.3*T <sub>clk</sub> ) / 7  | ps               | 2    |
| Effective time of LVDS                        |                | t <sub>eff</sub>     | ±360 |                              | ps               | -    |
| LVDS Clock to Clock Skew Margin (Even to Odd) |                | t <sub>SKEW EO</sub> |      | 1/7* T <sub>clk</sub>        | T <sub>clk</sub> | -    |

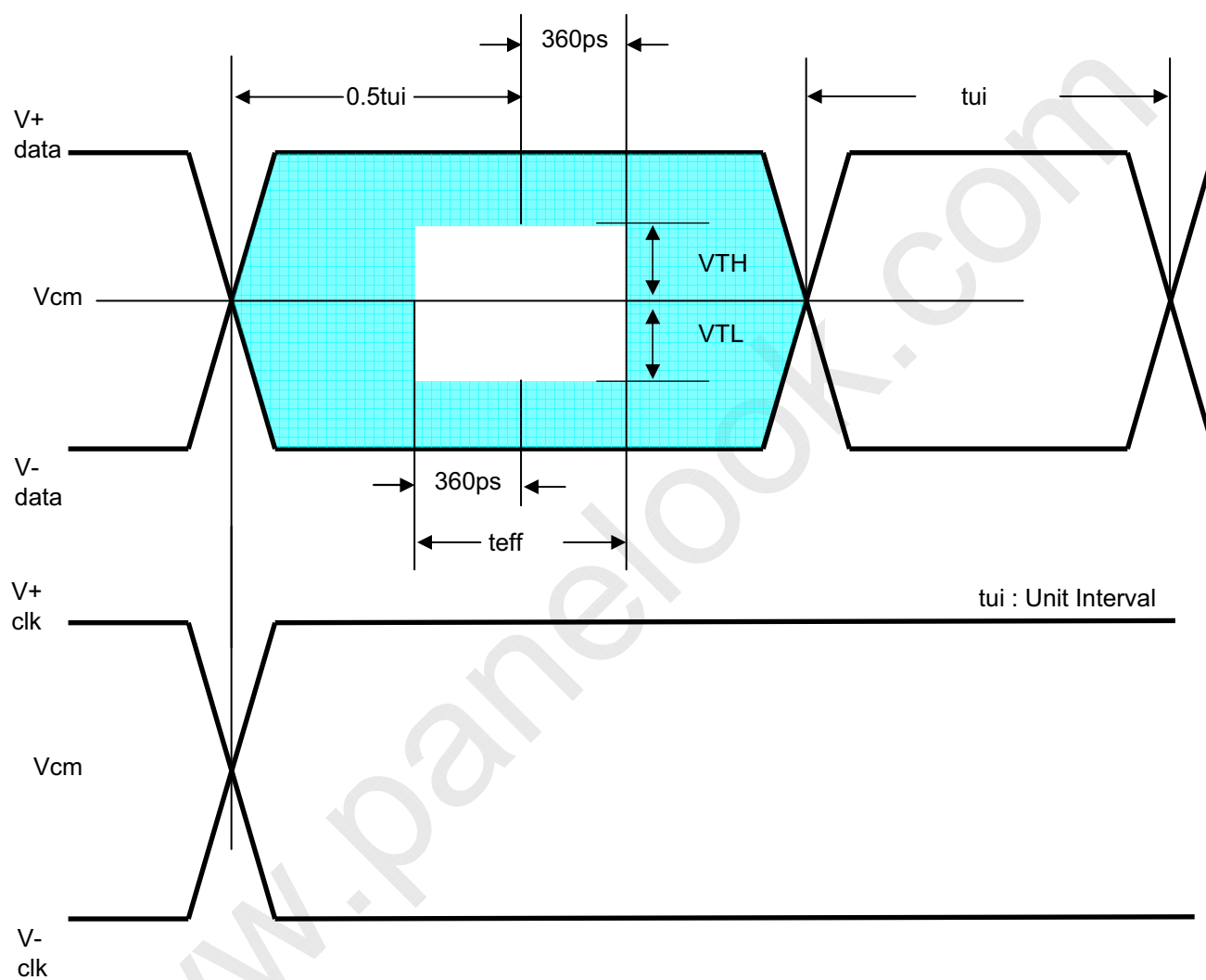
Note 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

2. If  $t_{RF}$  isn't enough,  $t_{eff}$  should be meet the range.

3. LVDS Differential Voltage is defined within  $t_{eff}$

LC420EUD

## Product Specification





LC420EUD

## Product Specification

## 3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 10-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

| Color       |              | Input Color Data |    |    |    |    |     |    |    |    |    |       |    |    |    |    |     |    |    |    |    |      |    |    |    |    |     |    |    |    |    |  |  |
|-------------|--------------|------------------|----|----|----|----|-----|----|----|----|----|-------|----|----|----|----|-----|----|----|----|----|------|----|----|----|----|-----|----|----|----|----|--|--|
|             |              | RED              |    |    |    |    |     |    |    |    |    | GREEN |    |    |    |    |     |    |    |    |    | BLUE |    |    |    |    |     |    |    |    |    |  |  |
|             |              | MSB              |    |    |    |    | LSB |    |    |    |    | MSB   |    |    |    |    | LSB |    |    |    |    | MSB  |    |    |    |    | LSB |    |    |    |    |  |  |
|             |              | R9               | R8 | R7 | R6 | R5 | R4  | R3 | R2 | R1 | R0 | G9    | G8 | G7 | G6 | G5 | G4  | G3 | G2 | G1 | G0 | B9   | B8 | B7 | B6 | B5 | B4  | B3 | B2 | B1 | B0 |  |  |
| Basic Color | Black        | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  |  |  |
|             | Red (1023)   | 1                | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  |  |  |
|             | Green (1023) | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 1     | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  |  |  |
|             | Blue (1023)  | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 1    | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  |  |  |
|             | Cyan         | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 1     | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  | 1    | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  |  |  |
|             | Magenta      | 1                | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 1    | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  |  |  |
|             | Yellow       | 1                | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  | 1     | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  |  |  |
|             | White        | 1                | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  | 1     | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  | 1    | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  |  |  |
| RED         | RED (000)    | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  |  |  |
|             | RED (001)    | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 1  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  |  |  |
|             | ...          |                  |    |    |    |    |     |    |    |    |    |       |    |    |    |    |     |    |    |    |    |      |    |    |    |    |     |    |    |    |    |  |  |
|             | RED (1022)   | 1                | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 0  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  |  |  |
|             | RED (1023)   | 1                | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  |  |  |
|             |              |                  |    |    |    |    |     |    |    |    |    |       |    |    |    |    |     |    |    |    |    |      |    |    |    |    |     |    |    |    |    |  |  |
| GREEN       | GREEN (000)  | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  |  |  |
|             | GREEN (001)  | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 1  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  |  |  |
|             | ...          |                  |    |    |    |    |     |    |    |    |    |       |    |    |    |    |     |    |    |    |    |      |    |    |    |    |     |    |    |    |    |  |  |
|             | GREEN (1022) | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 1     | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 0  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  |  |  |
|             | GREEN (1023) | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 1     | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  |  |  |
|             |              |                  |    |    |    |    |     |    |    |    |    |       |    |    |    |    |     |    |    |    |    |      |    |    |    |    |     |    |    |    |    |  |  |
| BLUE        | BLUE (000)   | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  |  |  |
|             | BLUE (001)   | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0    | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 1  | 0  |  |  |
|             | ...          |                  |    |    |    |    |     |    |    |    |    |       |    |    |    |    |     |    |    |    |    |      |    |    |    |    |     |    |    |    |    |  |  |
|             | BLUE (1022)  | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 1    | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 0  |  |  |
|             | BLUE (1023)  | 0                | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 0     | 0  | 0  | 0  | 0  | 0   | 0  | 0  | 0  | 0  | 1    | 1  | 1  | 1  | 1  | 1   | 1  | 1  | 1  | 1  |  |  |
|             |              |                  |    |    |    |    |     |    |    |    |    |       |    |    |    |    |     |    |    |    |    |      |    |    |    |    |     |    |    |    |    |  |  |



LC420EUD

## Product Specification

## 3-6. Power Sequence

## 3-6-1. LCD Driving circuit

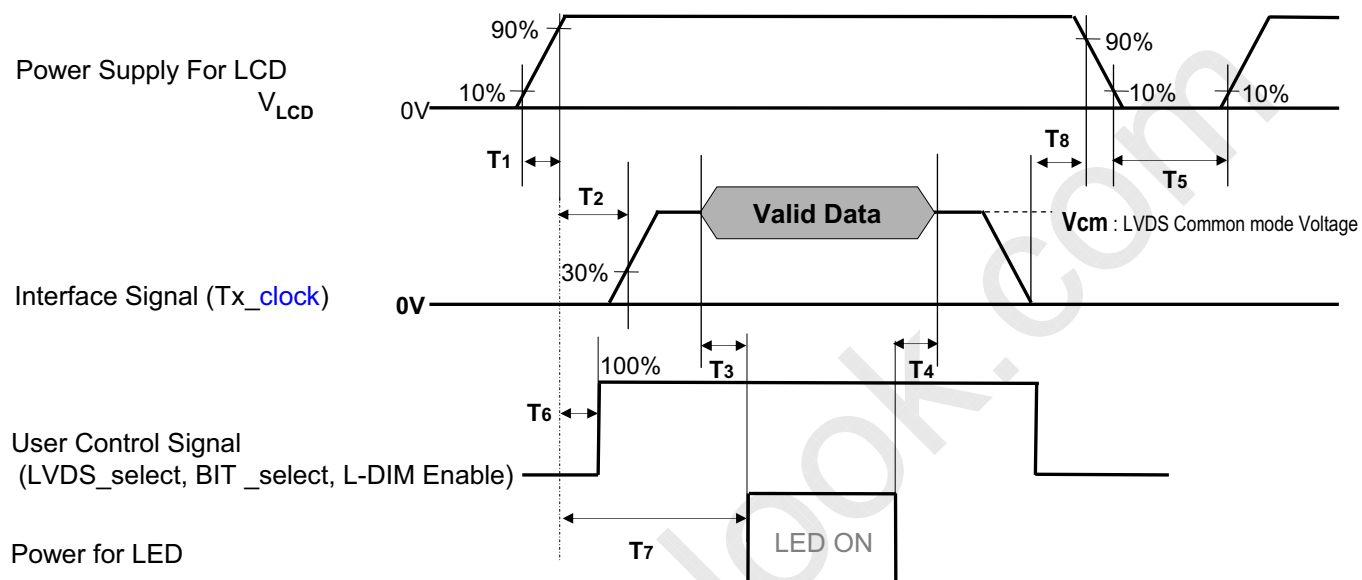


Table 8. POWER SEQUENCE

| Parameter | Value |     |     | Unit | Notes |
|-----------|-------|-----|-----|------|-------|
|           | Min   | Typ | Max |      |       |
| T1        | 0.5   | -   | 20  | ms   |       |
| T2        | 0     | -   | -   | ms   | 4     |
| T3        | 200   | -   | -   | ms   | 3     |
| T4        | 200   | -   | -   | ms   | 3     |
| T5        | 1.0   | -   | -   | s    | 5     |
| T6        | -     | -   | T2  | ms   | 4     |
| T7        | 0.5   | -   | -   | s    |       |
| T8        | 100   | -   | -   | ms   | 6     |

Note :1. Please avoid floating state of interface signal at invalid period.

2. When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.

3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.

4. If the on time of signals (Interface signal and user control signals) precedes the on time of Power ( $V_{LCD}$ ), it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.

5. T5 should be measured after the Module has been fully discharged between power off and on period.

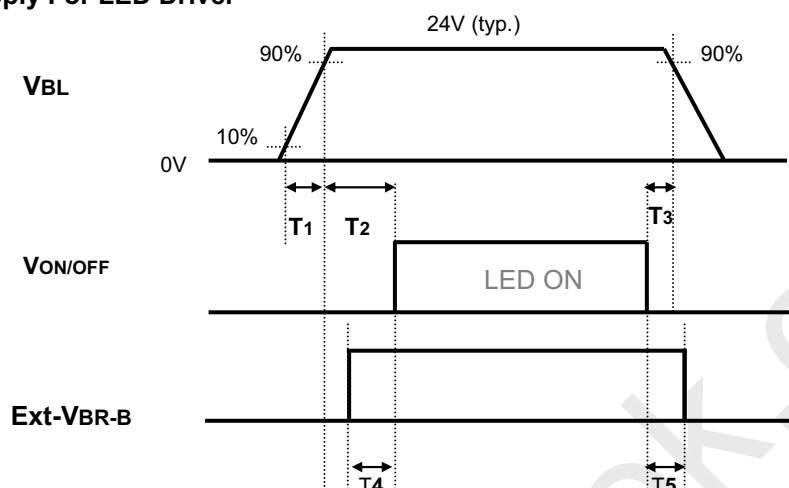
6. It is recommendation specification that T8 has to be 100ms as a minimum value.

LC420EUD

## Product Specification

## 3-6-2. Sequence for LED Driver

## Power Supply For LED Driver



## 3-6-3. Dip condition for LED Driver

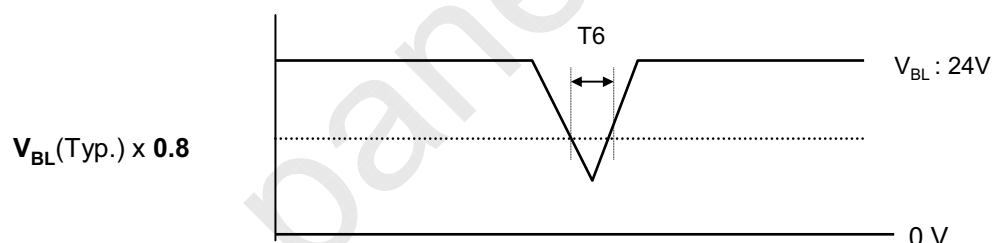


Table 9. Power Sequence for LED Driver

| Parameter | Values |     |     | Units | Remarks                  |
|-----------|--------|-----|-----|-------|--------------------------|
|           | Min    | Typ | Max |       |                          |
| T1        | 20     | -   | -   | ms    | 1                        |
| T2        | 500    | -   | -   | ms    |                          |
| T3        | 10     | -   | -   | ms    |                          |
| T4        | 0      | -   | -   | ms    |                          |
| T5        | 0      | -   | -   | ms    |                          |
| T6        | -      | -   | 10  | ms    | $V_{BL}(Typ) \times 0.8$ |

Notes : 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time.

Even though T1 is over the specified value, there is no problem if  $I^2T$  spec of fuse is satisfied.

LC420EUD

## Product Specification

## 4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at  $25\pm 2^{\circ}\text{C}$ . The values are specified at an approximate distance 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\theta$  equal to  $0^{\circ}$ .

It is presented additional information concerning the measurement equipment and method in FIG. 1.

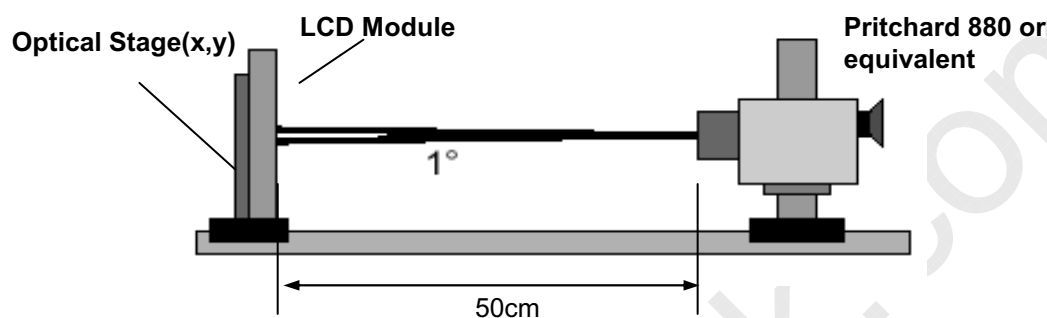


FIG. 1 Optical Characteristic Measurement Equipment and Method

$T_a = 25\pm 2^{\circ}\text{C}$ ,  $V_{\text{LCD}} = 12.0\text{V}$ ,  $f_v = 120\text{Hz}$ ,  $D_{\text{clk}} = 74.25\text{MHz}$ ,  
EXTV<sub>BR-B</sub> = 100%

Table 10. OPTICAL CHARACTERISTICS

| Parameter                      |                       | Symbol              |    | Value        |              |              | Unit              | Note |
|--------------------------------|-----------------------|---------------------|----|--------------|--------------|--------------|-------------------|------|
|                                |                       |                     |    | Min          | Typ          | Max          |                   |      |
| Contrast Ratio                 |                       | CR                  |    | 900          | 1300         | -            |                   | 1    |
| Surface Luminance, white       |                       | L <sub>WH</sub>     |    | 360          | 450          | -            | cd/m <sup>2</sup> | 2    |
| Luminance Variation            |                       | δ <sub>WHITE</sub>  | 5P | -            | -            | 1.3          |                   | 3    |
| Response Time                  | Gray-to-Gray          | G to G              |    | -            | 5            | 8            | ms                | 4,5  |
|                                | MPRT                  | MPRT                |    | -            | 8            | 12           | ms                |      |
|                                | Uniformity            | δ <sub>MPRT</sub>   |    | -            | -            | 1            |                   |      |
|                                | Uniformity            | δ <sub>G TO G</sub> |    | -            | -            | 1            |                   |      |
| Color Coordinates<br>[CIE1931] | RED                   | Rx                  |    | Typ<br>-0.03 | <b>0.647</b> | Typ<br>+0.03 |                   |      |
|                                |                       | Ry                  |    |              | <b>0.332</b> |              |                   |      |
|                                | GREEN                 | Gx                  |    |              | <b>0.309</b> |              |                   |      |
|                                |                       | Gy                  |    |              | <b>0.601</b> |              |                   |      |
|                                | BLUE                  | Bx                  |    |              | <b>0.149</b> |              |                   |      |
|                                |                       | By                  |    |              | <b>0.059</b> |              |                   |      |
|                                | WHITE                 | Wx                  |    |              | 0.279        |              |                   |      |
|                                |                       | Wy                  |    |              | 0.292        |              |                   |      |
| Color Temperature              |                       |                     |    |              | 10,000       |              | K                 |      |
| Color Gamut(CIE1931 NTSC)      |                       |                     |    |              | 72           |              | %                 |      |
| Viewing Angle (CR>10)          |                       |                     |    |              |              |              |                   |      |
|                                | x axis, right(φ=0°)   | θr                  |    | 89           | -            | -            | degree            | 6    |
|                                | x axis, left (φ=180°) | θl                  |    | 89           | -            | -            |                   |      |
|                                | y axis, up (φ=90°)    | θu                  |    | 89           | -            | -            |                   |      |
|                                | y axis, down (φ=270°) | θd                  |    | 89           | -            | -            |                   |      |
| Gray Scale                     |                       |                     |    | -            | -            | -            |                   | 7    |

Ver. 1.0

18 / 42



LC420EUD

## Product Specification

Note : 1. Contrast Ratio(CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

It is measured at center 1-point.

2. Surface luminance are determined after the unit has been 'ON' and 1 Hour after lighting the backlight in a dark environment at  $25 \pm 2^\circ\text{C}$ . Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.

3. The variation in surface luminance ,  $\delta$  WHITE is defined as :

$$\delta \text{ WHITE}(5P) = \text{Maximum}(L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5}) / \text{Minimum}(L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5})$$

Where  $L_{on1}$  to  $L_{on5}$  are the luminance with all pixels displaying white at 5 locations .

For more information, see the FIG. 2.

4. Response time is the time required for the display to transit from G(N) to G(M) (Rise Time,  $Tr_R$ ) and from G(M) to G(N) (Decay Time,  $Tr_D$ ). For additional information see the FIG. 3. ( $N < M$ )

※ G to G Spec stands for average value of all measured points.

Photo Detector : RD-80S / Field :  $2^\circ$

5. MPRT is defined as the 10% to 90% blur-edge width  $B_{ij}$ (pixels) and scroll speed  $U$ (pixels/frame)at the moving picture. For more information, see FIG 4

6. [Gray to Gray / MPRT Response time uniformity is Reference data.](#) **Appendix IV-1 / IV-2.**

7. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 5.

8. Gray scale specification

Gamma Value is approximately 2.2. For more information, see the Table 11.

**Table 11. GRAY SCALE SPECIFICATION**

| Gray Level | Luminance [%] (Typ) |
|------------|---------------------|
| L0         | 0.075               |
| L15        | 0.28                |
| L31        | 1.05                |
| L47        | 2.50                |
| L63        | 4.69                |
| L79        | 7.67                |
| L95        | 11.47               |
| L111       | 16.11               |
| L127       | 21.64               |
| L143       | 28.07               |
| L159       | 35.43               |
| L175       | 43.73               |
| L191       | 52.99               |
| L207       | 63.23               |
| L223       | 74.47               |
| L239       | 86.72               |
| L255       | 100                 |

LC420EUD

## Product Specification

Measuring point for surface luminance & measuring point for luminance variation.

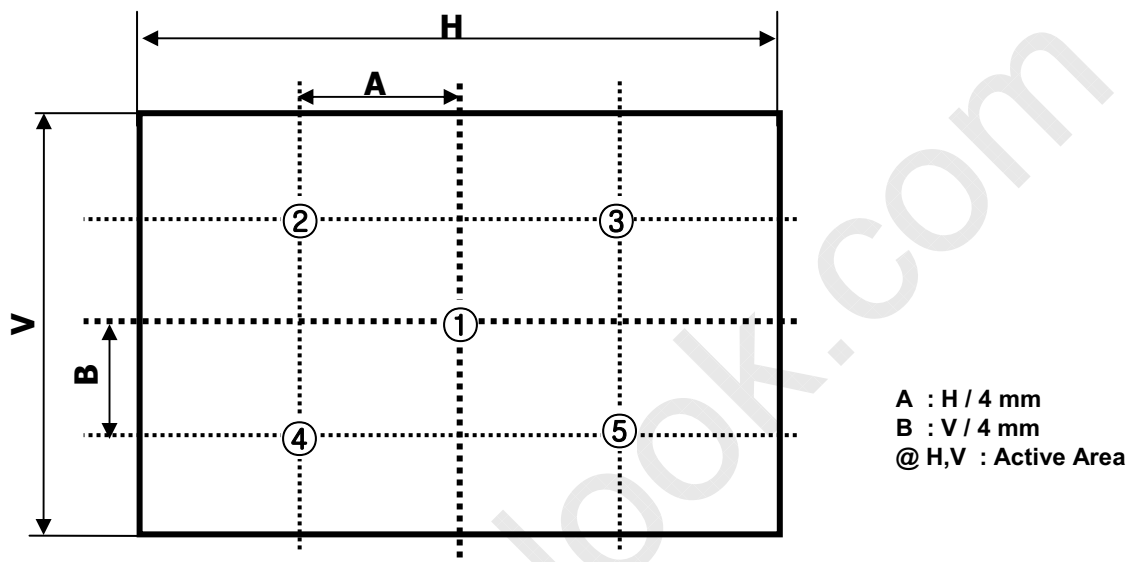


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

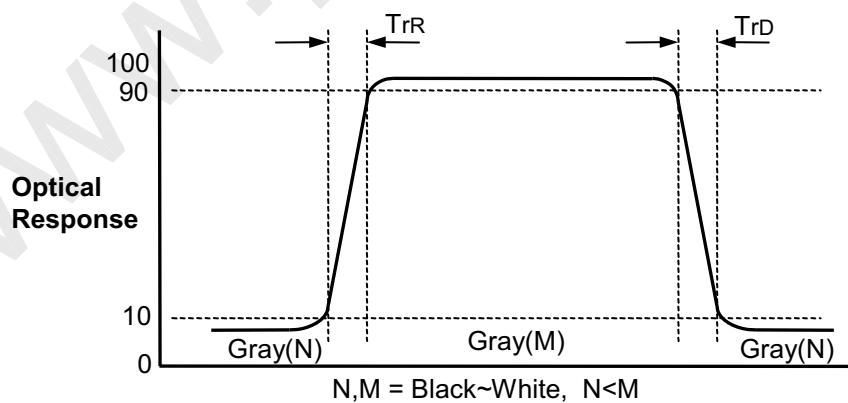


FIG. 3 Response Time

LC420EUD

## Product Specification

MPRT is defined as the 10% to 90% blur-edge with  $B_{ij}$ (pixels) and scroll speed  $U$ (pixels/frame)at the moving picture.

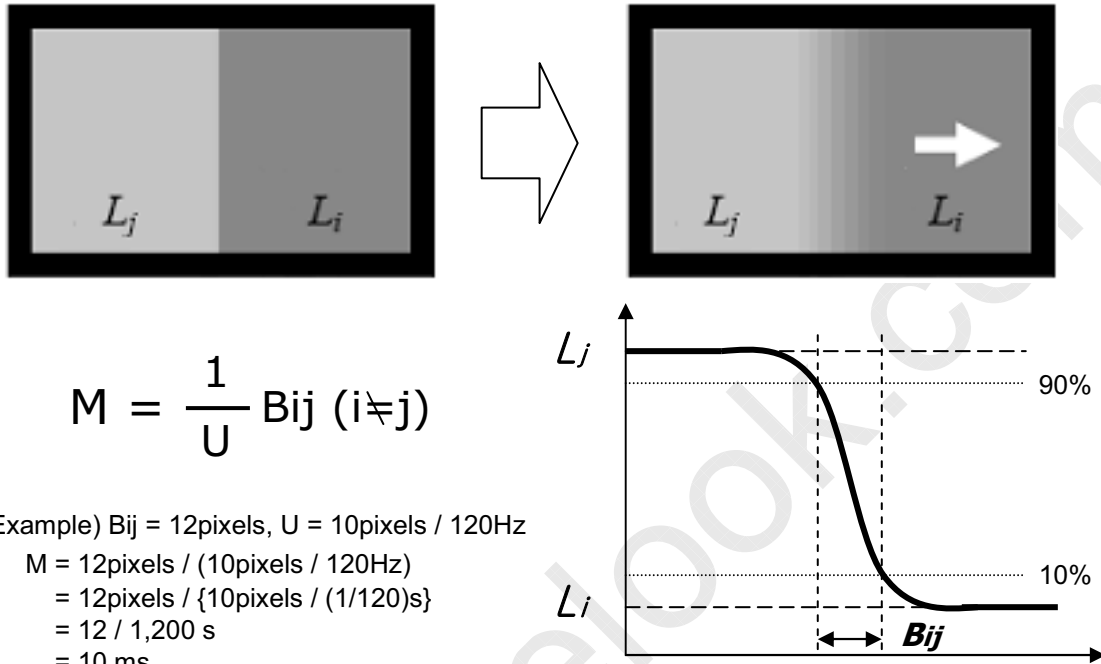


FIG. 4 MPRT

Dimension of viewing angle range

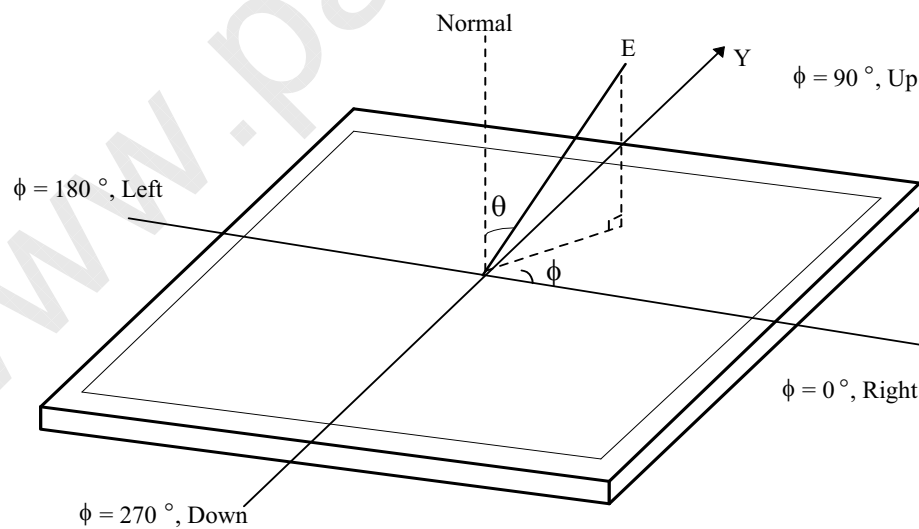


FIG. 5 Viewing Angle



LC420EUD

## Product Specification

## 5. Mechanical Characteristics

Table 10 provides general mechanical characteristics.

**Table 10. MECHANICAL CHARACTERISTICS**

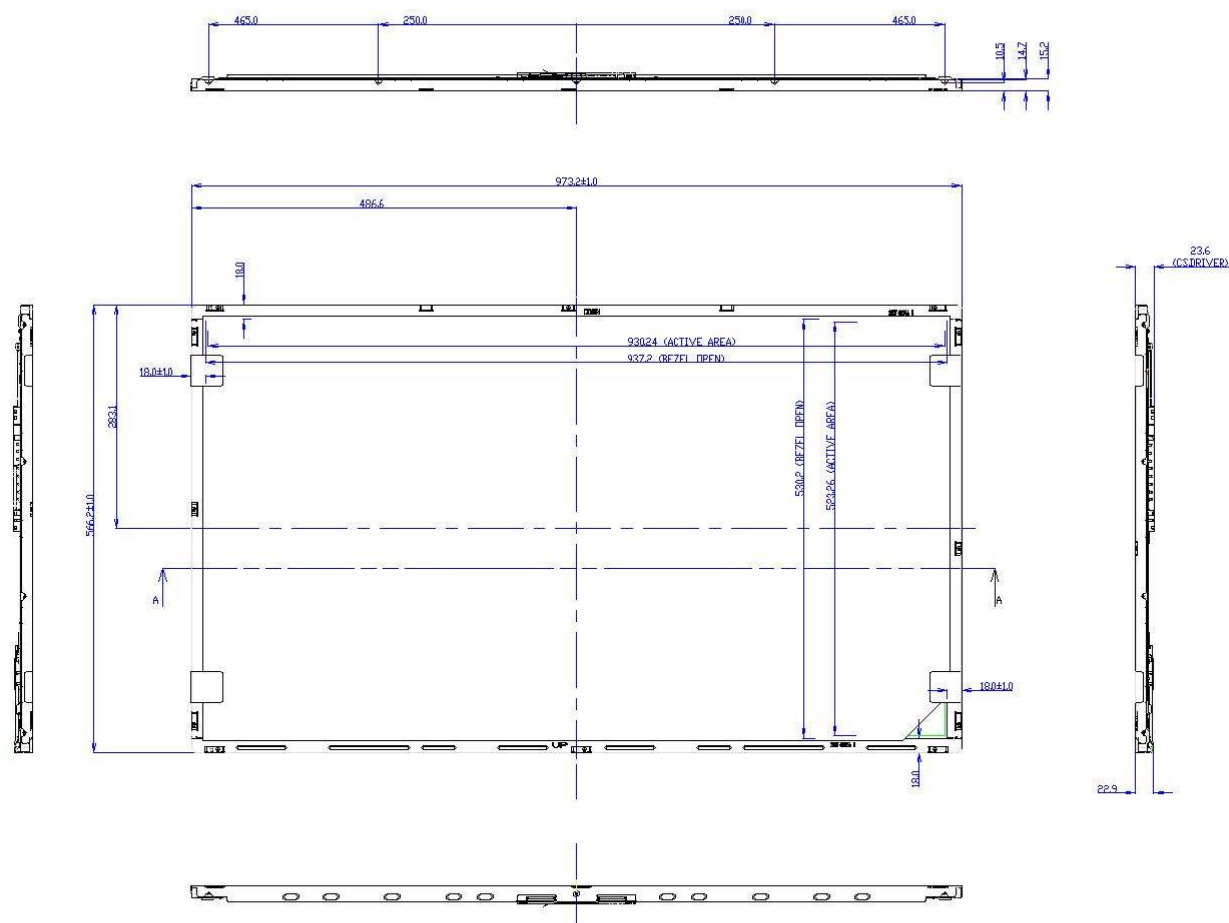
| Item                | Value                     |           |
|---------------------|---------------------------|-----------|
| Outline Dimension   | Horizontal                | 973.2 mm  |
|                     | Vertical                  | 566.2 mm  |
|                     | Depth                     | 10.8 mm   |
| Bezel Area          | Horizontal                | 937.2 mm  |
|                     | Vertical                  | 530.2 mm  |
| Active Display Area | Horizontal                | 930.24 mm |
|                     | Vertical                  | 523.26 mm |
| Weight              | 11.3 Kg(Typ), 12.0Kg(Max) |           |

Note : Please refer to a mechanical drawing in terms of tolerance at the next page.

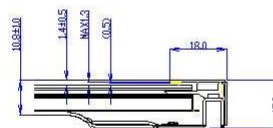
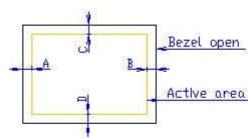
## &lt;Front View&gt;

LC420EUD

## Product Specification



NOTES:  
1) UNSPECIFIED TOLERANCE IS ±1.0MM.  
2) TILT AND PARTIAL DISPOSITION TOLERANCE OF DISPLAY AREA AS FOLLOWING.  
① Y-DIRECTION I A-B I ≤ 1.5  
② X-DIRECTION I C-D I ≤ 1.5  
3) M3.0, M4.0 TAP: Max Torque 6kgf.cm



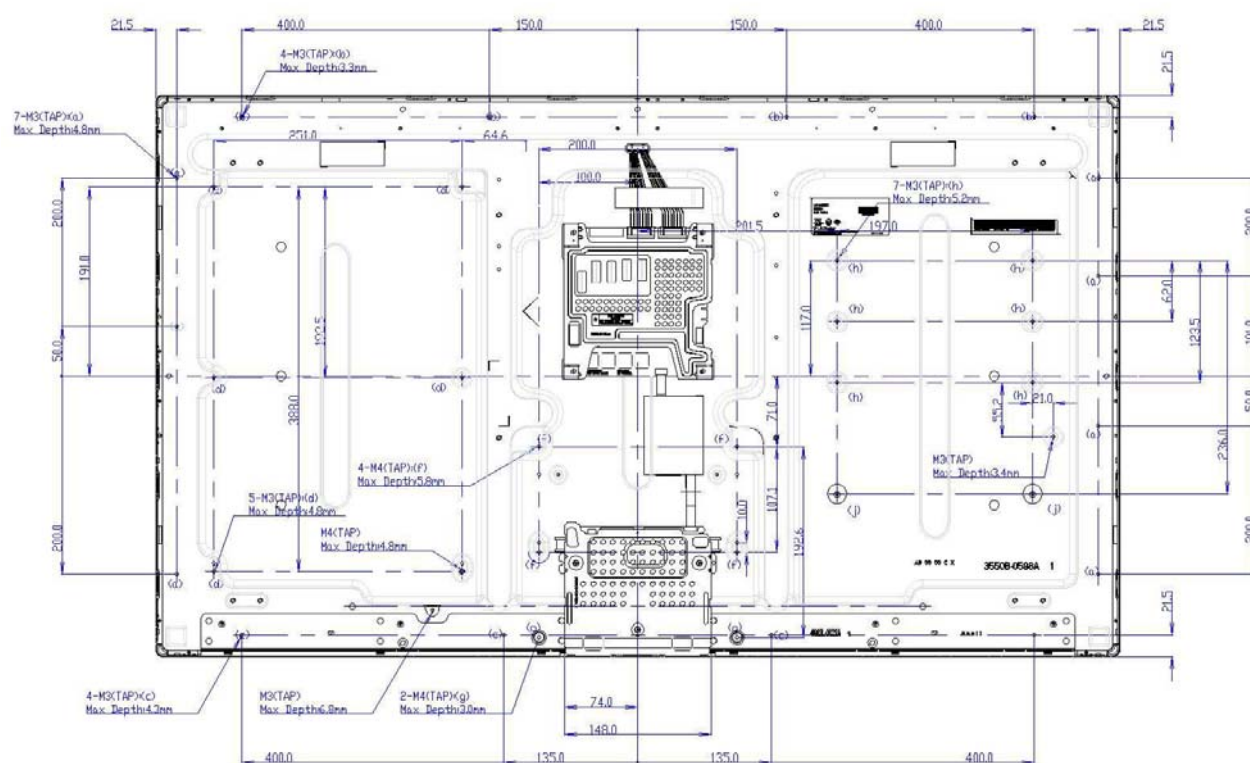
SECTION A-A  
SCALE 1/1



## &lt;Rear View&gt;

LC420EUD

## Product Specification





LC420EUD

## Product Specification

## 6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

| No. | Test Item                                    | Condition                                                                                                                                 |
|-----|----------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------|
| 1   | High temperature storage test                | Ta= 60°C 240h                                                                                                                             |
| 2   | Low temperature storage test                 | Ta= -20°C 240h                                                                                                                            |
| 3   | High temperature operation test              | Ta= 50°C 50%RH 240h                                                                                                                       |
| 4   | Low temperature operation test               | Ta= 0°C 240h                                                                                                                              |
| 5   | Vibration test<br>(non-operating)            | Wave form : random<br>Vibration level : 1.0Grms<br>Bandwidth : 10-300Hz<br>Duration : X,Y,Z, 30 min<br>Each direction per 10 min          |
| 6   | Shock test<br>(non-operating)                | Shock level : 50Grms,35G(Z axis)<br>Waveform : half sine wave, 11ms<br>Direction : $\pm X$ , $\pm Y$ , $\pm Z$<br>One time each direction |
| 7   | Humidity condition Operation                 | Ta= 40 °C ,90%RH                                                                                                                          |
| 8   | Altitude     operating<br>storage / shipment | 0 - 15,000 ft<br>0 - 40,000 ft                                                                                                            |

Note : Before and after Reliability test, LCM should be operated with normal function.



LC420EUD

## Product Specification

## 7. International Standards

### 7-1. Safety

- a) UL 60065, Seventh Edition, Underwriters Laboratories Inc.  
Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- b) CAN/CSA C22.2 No.60065:03, Canadian Standards Association.  
Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- c) EN 60065:2002 + A11:2008, European Committee for Electrotechnical Standardization (CENELEC).  
Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- d) IEC 60065:2005 + A1:2005, The International Electrotechnical Commission (IEC).  
Audio, Video and Similar Electronic Apparatus - Safety Requirements.  
(Including report of IEC60825-1:2001 clause 8 and clause 9)

#### Notes

##### 1. Laser (LED Backlight) Information

|                                                                          |
|--------------------------------------------------------------------------|
| Class 1 LED Product<br>IEC60825-1 : 2001<br>Embedded LED Power (Class 1) |
|--------------------------------------------------------------------------|

### 7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment – Radio disturbance characteristics – Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment – Radio disturbance characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

### 7-3. Environment

- a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

LC420EUD

## Product Specification

## 8. Packing

### 8-1. Information of LCM Label

#### a) Lot Mark

|   |   |   |   |   |   |   |   |   |   |   |   |   |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| A | B | C | D | E | F | G | H | I | J | K | L | M |
|---|---|---|---|---|---|---|---|---|---|---|---|---|

A,B,C : SIZE(INCH)  
E : MONTH

D : YEAR  
F ~ M : SERIAL NO.

#### Note

##### 1. YEAR

|      |      |      |      |      |      |      |      |      |      |      |
|------|------|------|------|------|------|------|------|------|------|------|
| Year | 2001 | 2002 | 2003 | 2004 | 2005 | 2006 | 2007 | 2008 | 2009 | 2010 |
| Mark | 1    | 2    | 3    | 4    | 5    | 6    | 7    | 8    | 9    | 0    |

##### 2. MONTH

|       |     |     |     |     |     |     |     |     |     |     |     |     |
|-------|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
| Mark  | 1   | 2   | 4   | 4   | 5   | 6   | 7   | 8   | 9   | A   | B   | C   |

##### 3. FACTORY CODE

|              |      |         |      |
|--------------|------|---------|------|
| Factory Code | Gumi | Nanjing | Paju |
| Mark         | K    | C       | P    |

#### b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module.  
This is subject to change without prior notice.

### 8-2. Packing Form

a) Package quantity in one pallet : 15 ea

b) Pallet Size : 1140 mm X 990 mm X 798mm



LC420EUD

## Product Specification

## 9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

### 9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

### 9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :  
 $V = \pm 200\text{mV}$  (Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)  
And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.  
(if not, it can cause conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) The conductive material and signal cables are kept away from LED driver inductor to prevent abnormal display, sound noise and temperature rising.



LC420EUD

## Product Specification

### 9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

### 9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

### 9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.  
It is recommended that they be stored in the container in which they were shipped.

### 9-6. Handling Precautions for Protection Film

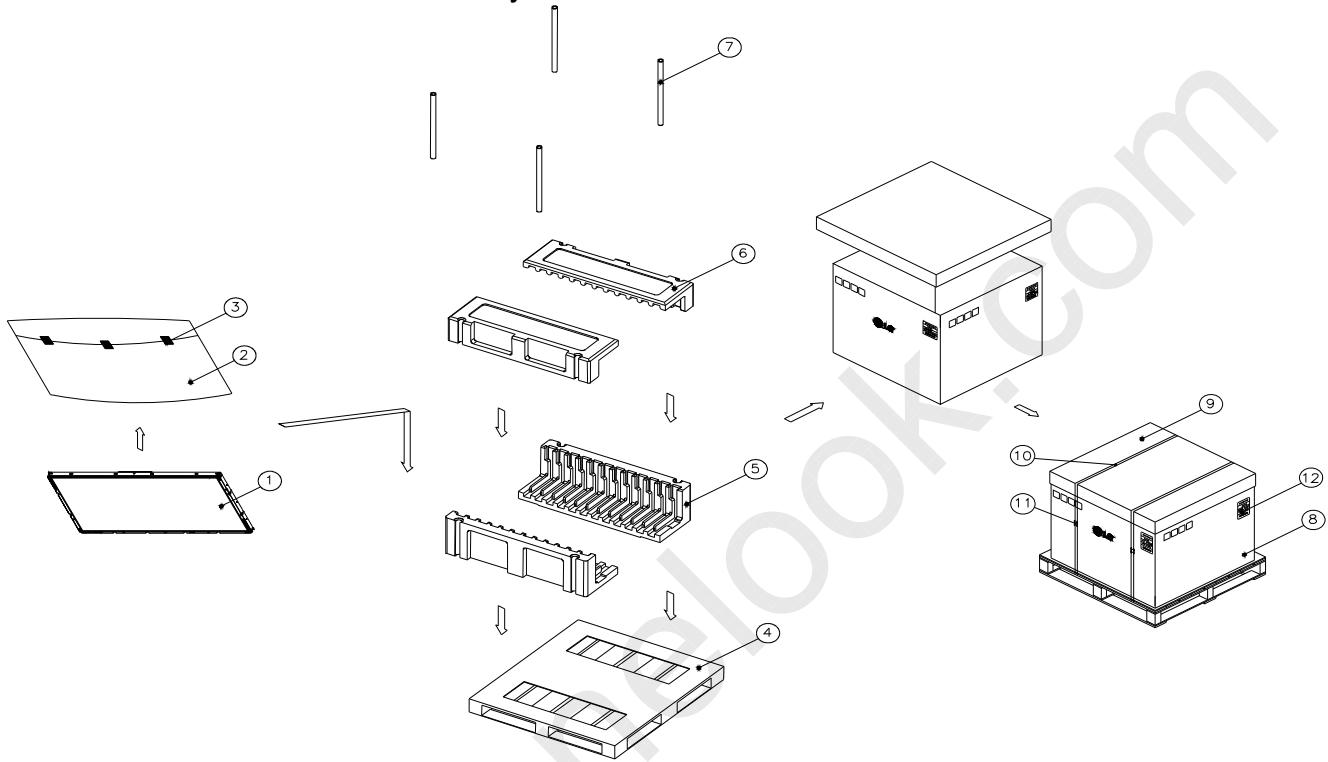
- (1) The protection film is attached to the bezel with a small masking tape.  
When the protection film is peeled off, static electricity is generated between the film and polarizer.  
This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

LC420EUD

## Product Specification

## # APPENDIX-I

## ■ LC420EUD-SCA2 – Pallet Ass'y



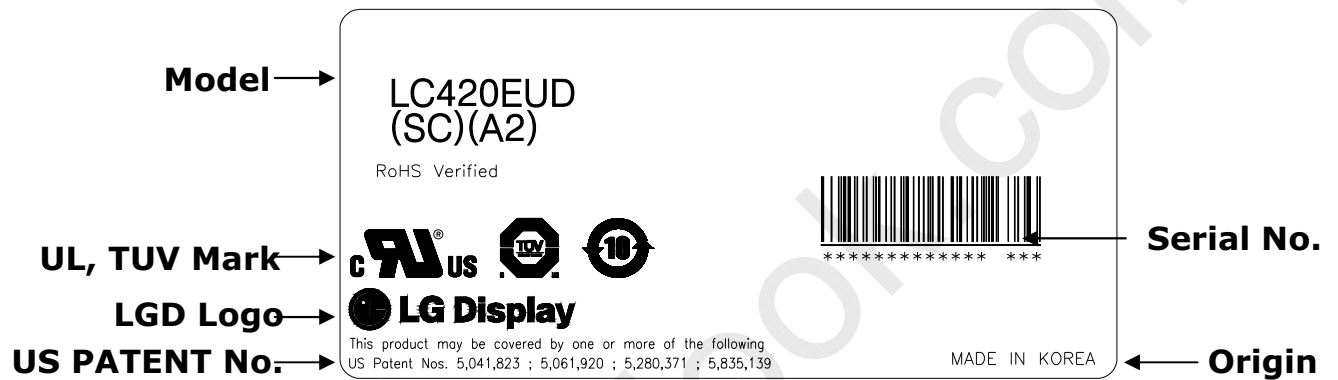
| NO. | DESCRIPTION    | MATERIAL                 |
|-----|----------------|--------------------------|
| 1   | LCD Module     |                          |
| 2   | BAG            | 42INCH                   |
| 3   | TAPE           | MASKING 20MMX50M         |
| 4   | PALLET         | Plywood 1140X990X125.5mm |
| 5   | PACKING,BOTTOM | EPS                      |
| 6   | PACKING, TOP   | EPS                      |
| 7   | ANGLE,POST     | PAPER                    |
| 8   | ANGLE,PACKING  | PAPER                    |
| 9   | ANGLE.COVER    | PAPER                    |
| 10  | BAND,CLIP      | STEEL or PP              |
| 11  | BAND           | PP                       |
| 12  | LABEL          | YUPO 80G 100X70          |

LC420EUD

## Product Specification

## # APPENDIX- II-1

## ■ LC420EUD-SCA2-LCM Label





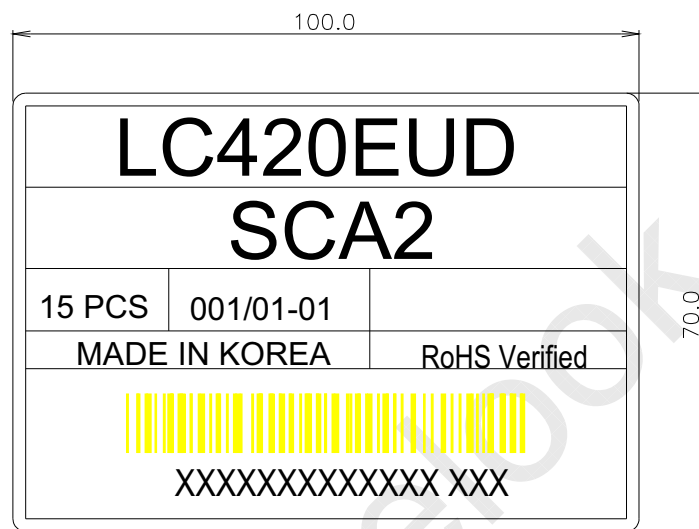


LC420EUD

Product Specification

# APPENDIX- II-2

■ LC420EUD-SCA2-Pallet Label



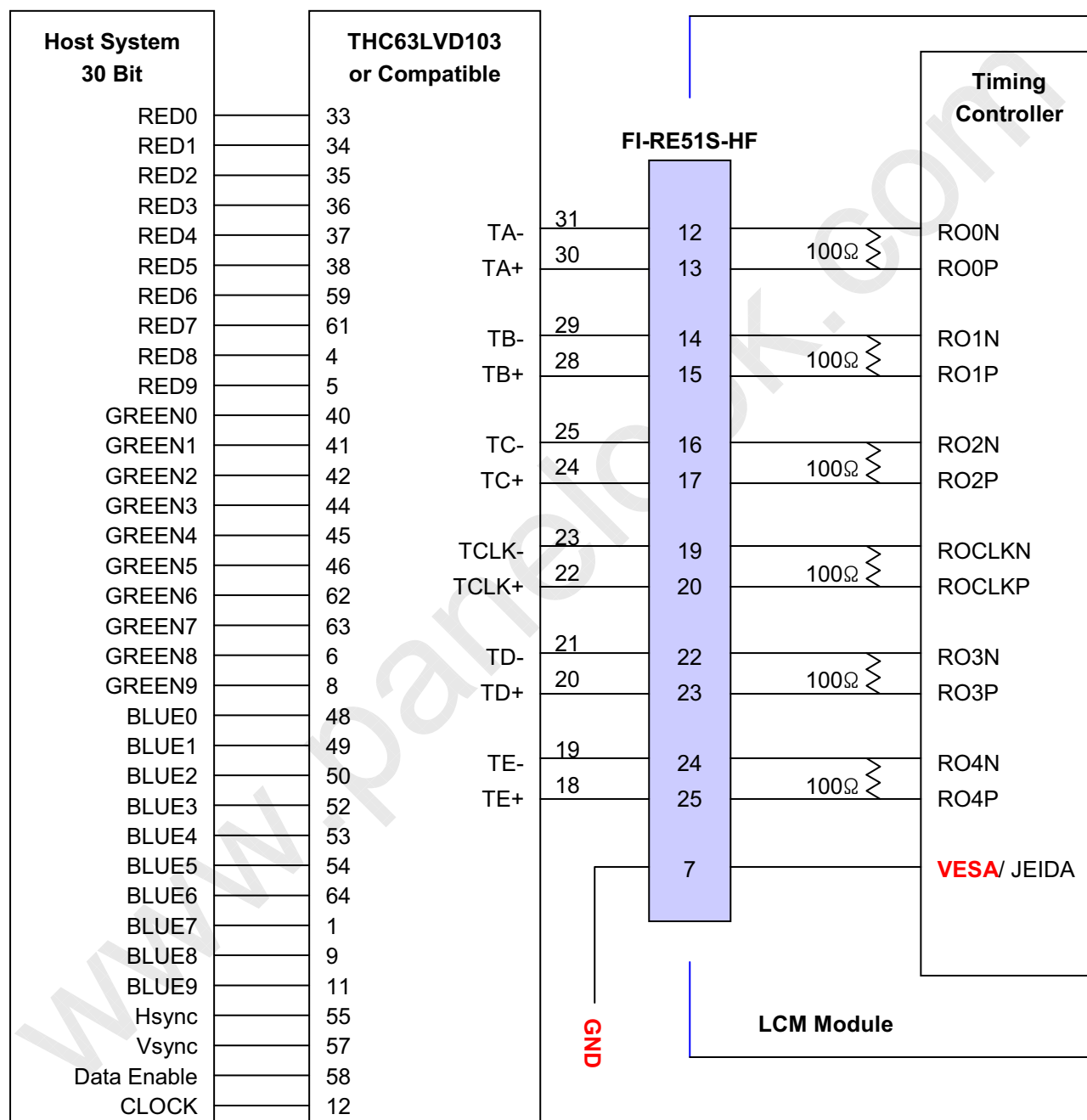


LC420EUD

## Product Specification

## # APPENDIX- III-1

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="L")



Note: 1. The LCD module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.

2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

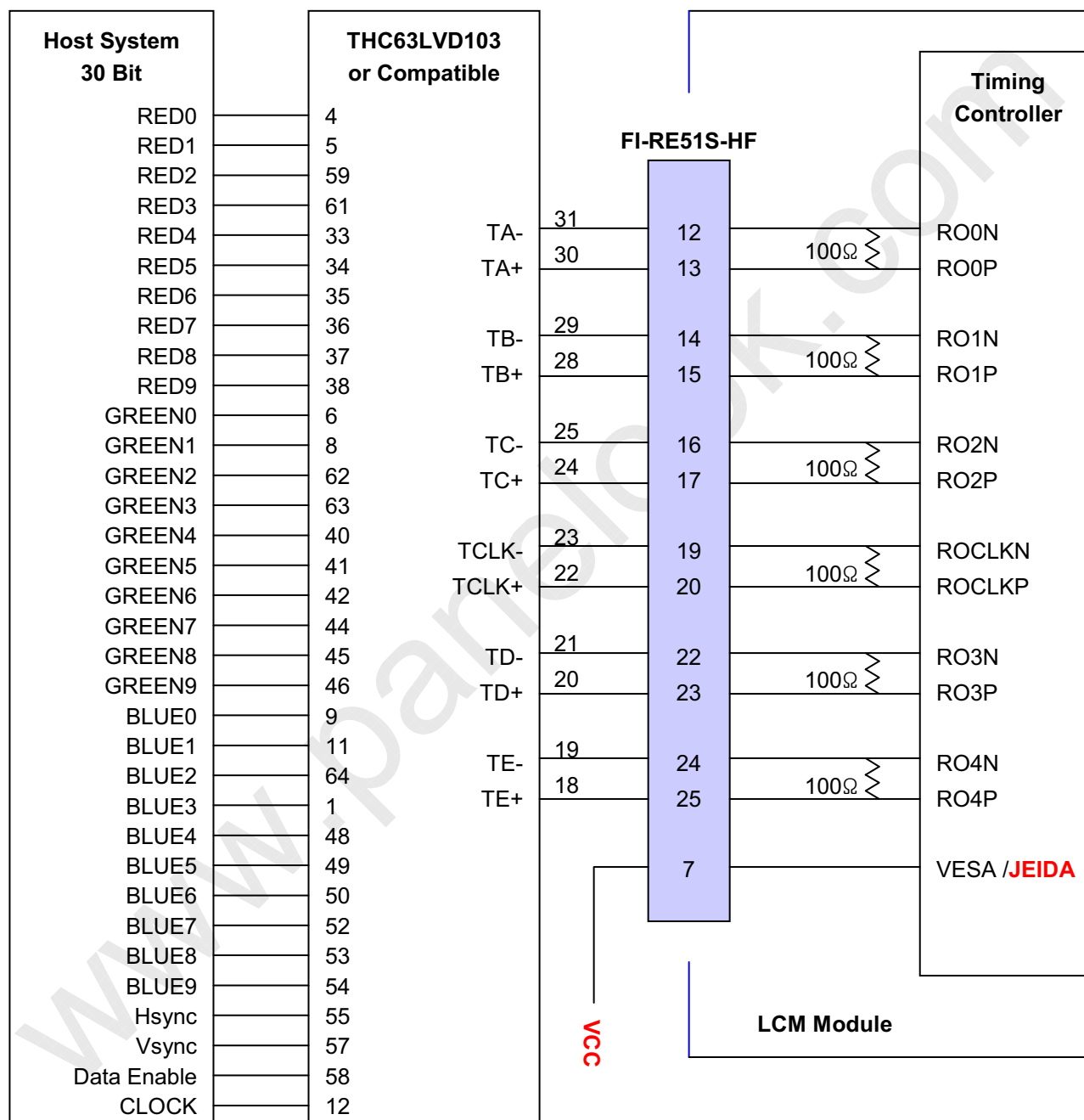
3. '9' means MSB and '0' means LSB at R,G,B pixel data.

LC420EUD

## Product Specification

## # APPENDIX- III-1

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="H")



Note :1. The LCD module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.

2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

3. '9' means MSB and '0' means LSB at R,G,B pixel data.

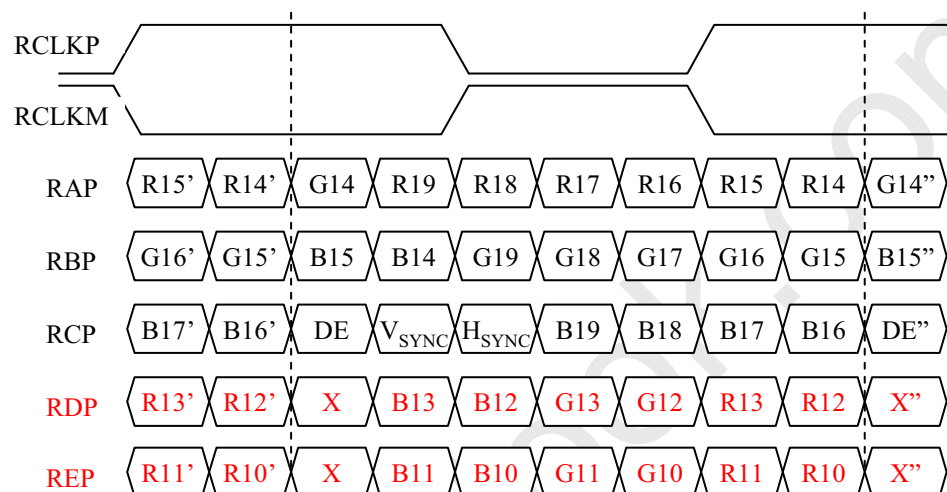
LC420EUD

## Product Specification

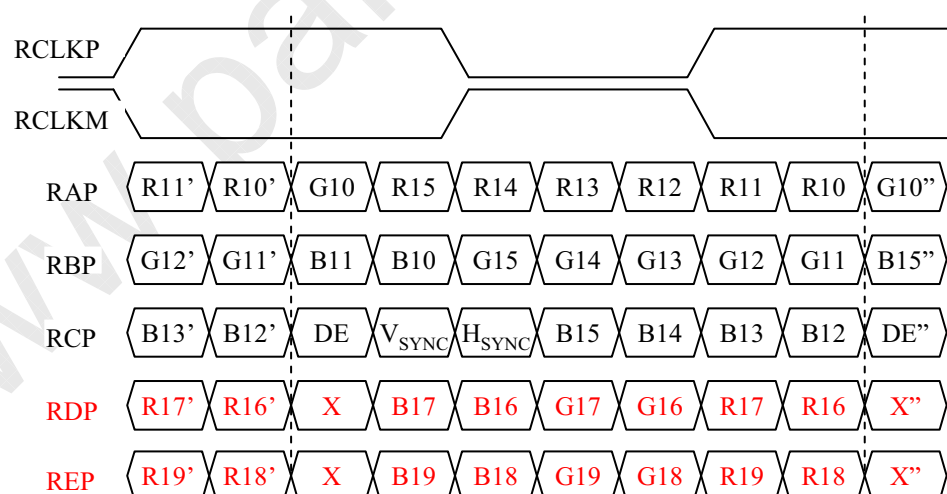
## # APPENDIX- III-2

## ■ LVDS Data-Mapping Information (10 Bit )

## 1) LVDS Select : "H" Data-Mapping (JEIDA format)



## 2) LVDS Select : "L" Data-Mapping (VESA format)



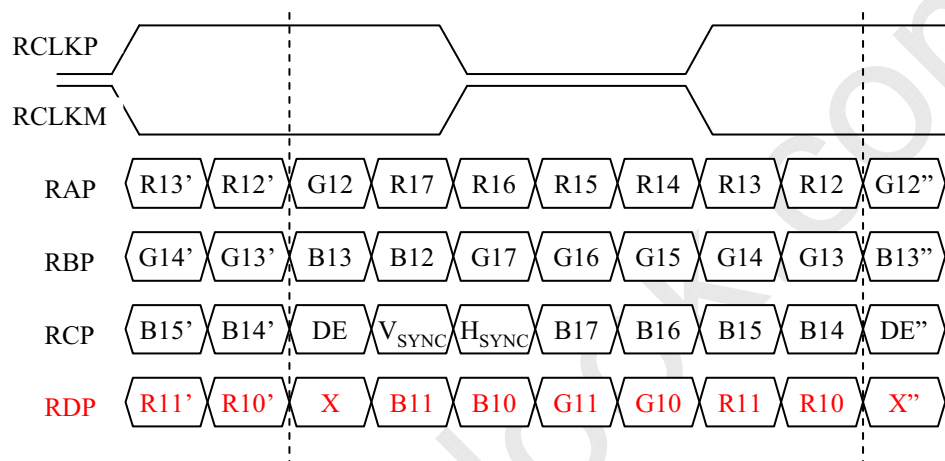
LC420EUD

## Product Specification

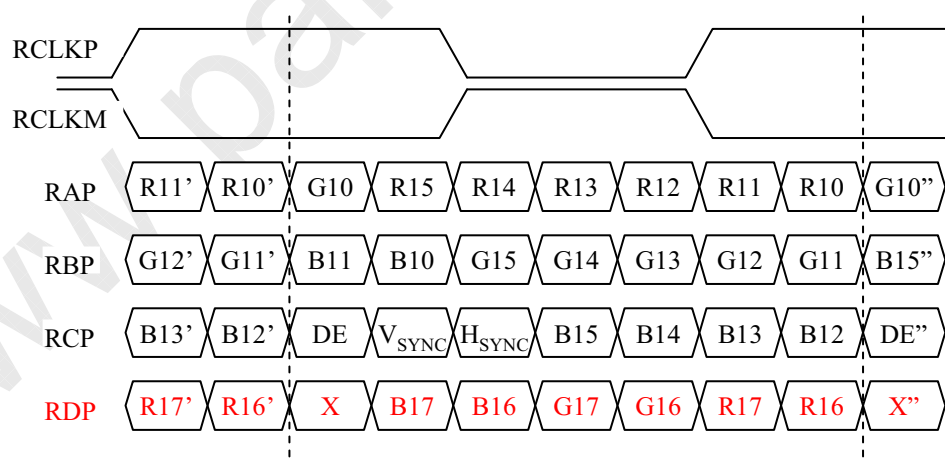
## # APPENDIX- III-2

## ■ LVDS Data-Mapping Information (8 Bit )

## 1) LVDS Select : "H" Data-Mapping (JEIDA format)



## 2) LVDS Select : "L" Data-Mapping (VESA format)

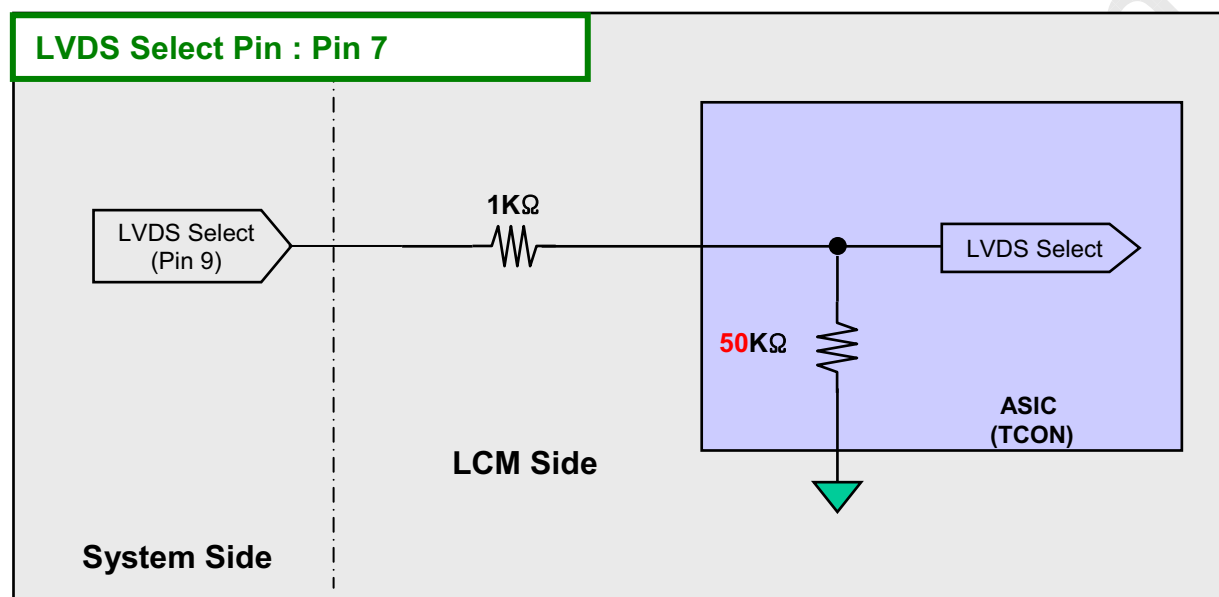
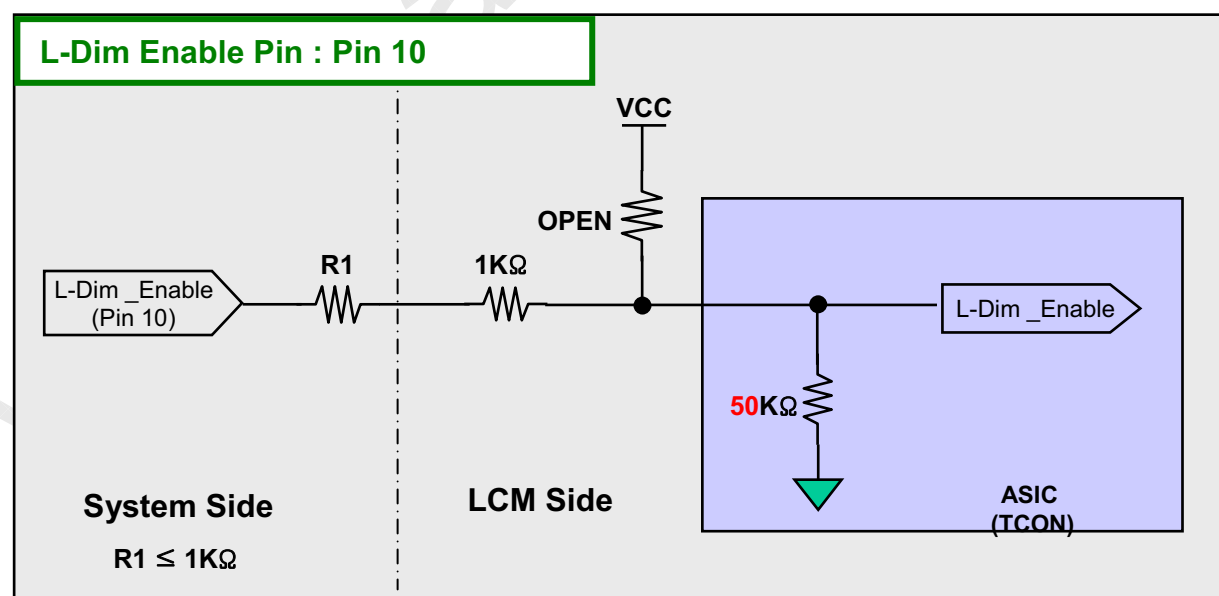


LC420EUD

## Product Specification

## # APPENDIX- III-3

## ■ Option Pin Circuit Block Diagram

1) Circuit Block Diagram of **LVDS Format** Selection pin2) Circuit Block Diagram of **L-Dim Enable** Selection pin

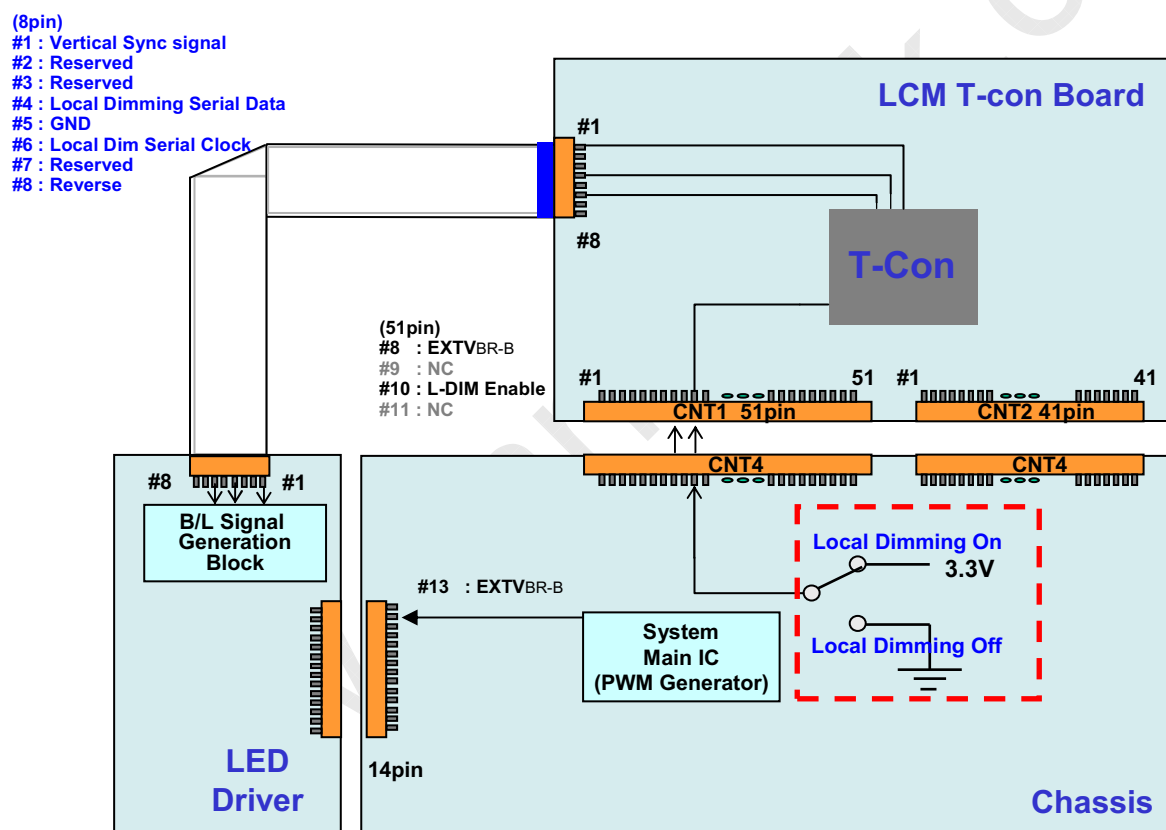
LC420EUD

## Product Specification

## # APPENDIX- III-4

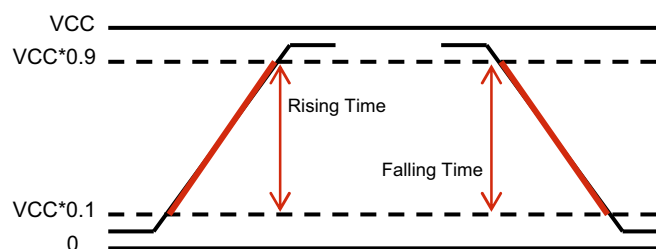
■ EXT<sub>VB</sub>R-B & Local Dimming Design Guide

- 1) When L-Dim Enable is "L", Vertical Sync Signal = System Dimming with 100Hz or 120Hz frequency.
- 2) Local Dimming signals are synchronized with V-Sync Freq. of System in T-Con Board.
- 3) EXT<sub>VB</sub>R-B Specification ( VCC = 3.3V ) @ Local Dimming
  - a) High Voltage Range : 2.5 V ~ 3.6 V
  - b) Low Voltage Range : 0.0 V ~ 0.8 V



&lt;With Driver Model&gt;

|                                 |                                                               |
|---------------------------------|---------------------------------------------------------------|
| EXT <sub>VB</sub> R-B Frequency | MAX 1KHz<br>Recommendation: 100 Hz for PAL<br>120 Hz for NTSC |
| Rising Time                     | MAX 10.0 $\mu$ s                                              |
| Falling Time                    | MAX 10.0 $\mu$ s                                              |



Ver. 1.0

38 / 42

LC420EUD

## Product Specification

## # APPENDIX- IV-1

## Gray to Gray Response Time Uniformity

This is only the reference data of G to G and uniformity for LC420EUD-SCA2 model.

## 1. G to G Response Time :

Response time is defined as Figure3 and shall be measured by switching the input signal for "Gray (N) " and "Gray(M)".(32Gray Step at 8bit)

## 2. G to G Uniformity

The variation of G to G Uniformity ,  $\delta_{G \rightarrow G}$  is defined as :

$$G \text{ to } G \text{ Uniformity} = \frac{\text{Maximum}(G \rightarrow G) - \text{Typical}(G \rightarrow G)}{\text{Typical}(G \rightarrow G)} \leq 1$$

\*Maximum (GtoG) means maximum value of measured time (N, M = 0 (Black) ~ 1023(White), 128 gray step).

|          | 0Gray        | 127ray         | 255Gray        | ... | 895Gray        | 1023Gray       |
|----------|--------------|----------------|----------------|-----|----------------|----------------|
| 0Gray    |              | TrR:0G→127G    | TrR:0G→255G    | ... | TrR:0G→895G    | TrR:0G→1023G   |
| 127Gray  | TrD:127G→0G  |                | TrR:127G→255G  | ... | TrR:127G→895G  | TrR:127G→1023G |
| 255Gray  | TrD:255G→0G  | TrD:255G→127G  |                | ... | TrR:255G→895G  | TrR:255G→1023G |
| ...      | ...          | ...            | ...            | ... | ...            | ...            |
| 895Gray  | TrD:895G→0G  | TrD:895G→127G  | TrD:895G→255G  | ... |                | TrR:895G→1023G |
| 1023Gray | TrD:1023G→0G | TrD:1023G→127G | TrD:1023G→255G | ... | TrD:1023G→895G |                |

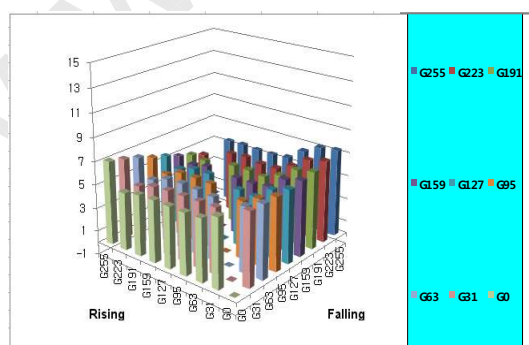
## 3. Sampling Size : 2 pcs

## 4. Measurement Method : Follow the same rule as optical characteristics measurement.

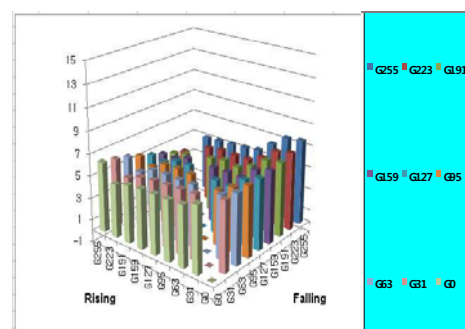
## 5. Current Status

Below table is actual data of production on Mar. 09. 2010

|     | G to G Response Time [ms] |      | Uniformity |
|-----|---------------------------|------|------------|
|     | Min.                      | Max. |            |
| # 1 | 4.24                      | 7.42 | 0.57       |
| # 2 | 3.82                      | 7.65 | 0.69       |



&lt; # 1 &gt;



&lt; # 2 &gt;



LC420EUD

## Product Specification

## # APPENDIX- IV-2

■ MPRT Response Time Uniformity ( $\delta_{\text{MPRT}}$ )

This is only the reference data of MPRT and uniformity for LC420EUD-SCA2 model.

## 1. MPRT Response Time :

Response time is defined as Figure3

## 2. MPRT Uniformity

The variation of MPRT Uniformity ,  $\delta_{\text{MPRT}}$  is defined as :

$$\text{MPRT Uniformity} = \frac{\text{Maximum (MPRT)} - \text{Typical (MPRT)}}{\text{Typical (MPRT)}} \leq 1$$

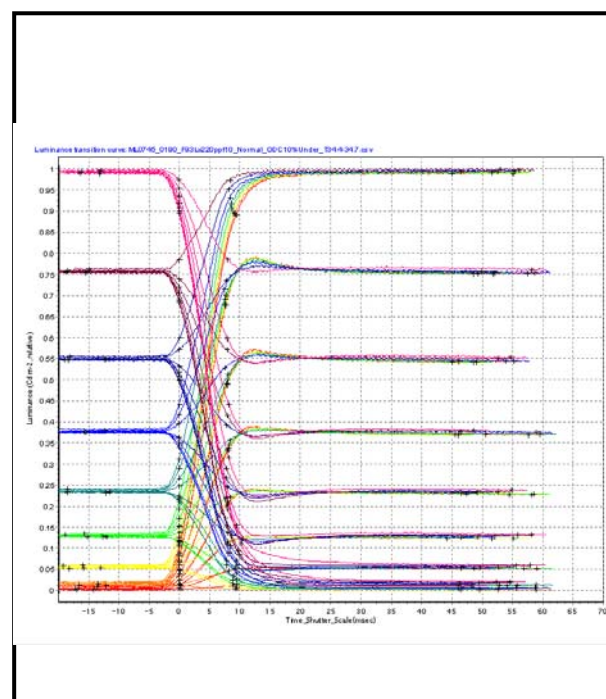
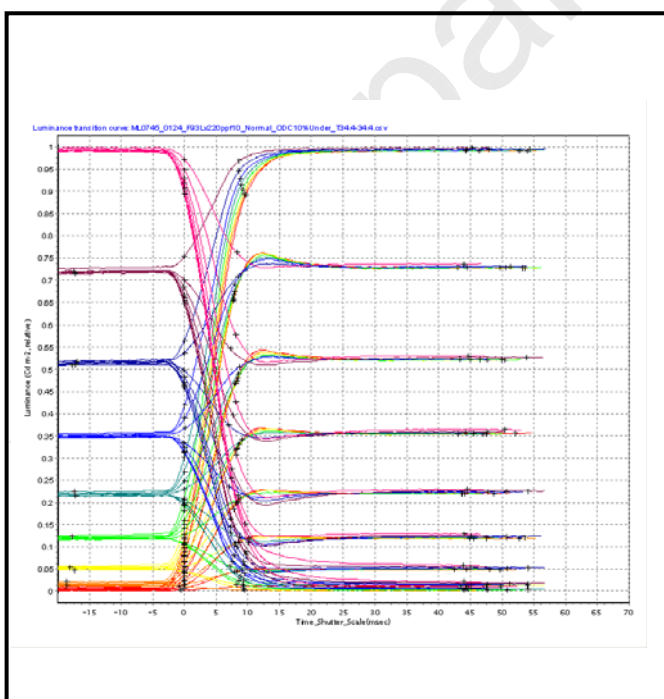
## 3. Sampling Size : 2 pcs

## 4. Measurement Method : Follow the same rule as optical characteristics measurement.

## 5. Current Status

Below table is actual data of production on XXX **Mar. 09. 2010**

| Sample | MPRT Response Time [ms] |      | Uniformity |
|--------|-------------------------|------|------------|
|        | Min.                    | Max. |            |
| # 1    | 7.05                    | 10.2 | 0.39       |
| # 2    | 7.53                    | 10.3 | 0.34       |



Ver. 1.0

40 / 42

LC420EUD

## Product Specification

## # APPENDIX- V

## Local Dimming Interface Design Guide

## ► Data Sequence (※ based on 12 block )

8-bit : Indicator(1010\_1010) / Command(8-bit) / Data1(8-bit) / Data2(8-bit) / ... / Data12(8-bit) / check\_Sum(8-bit)

## ► Data field Definition (※ based on 12 block )

1. Indicator Byte : Start of data sequence

2. Command Byte

- Bit 0 : Local-Dimming Enable ( '1' : Enable, '0' : Disable )

- Bit 1 : Reserved (Must be Low Level ('0'))

- Bit 2 : Reserved (Must be High Level ('1'))

- Bit 3~6 : Reserved (Must be Low Level ('0'))

- Bit 7 : Reverse Enable ( '1' : Enable (Reverse), '0' : Disable (Normal) )

3. Data Byte 1 ~ 12 : 8-bit Local-dimming gray value

4. Check\_Sum Byte = Indicator ^ Command ^ Data1 ^ Data2 ^ ... Data12 (※ ^ : Exclusive OR)

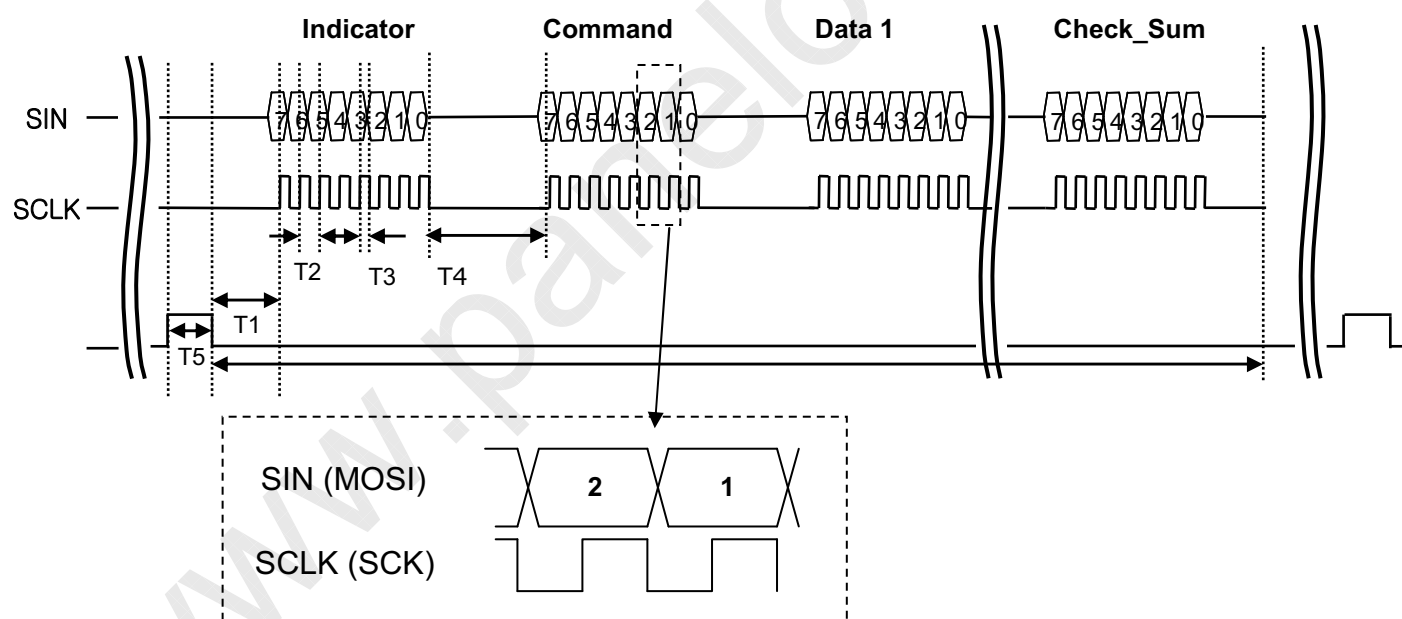


Table15. TIMING TABLE for Local Dimming Interface

※ SPI Clock Range : Min 100 [KHz], Max 500 [KHz]

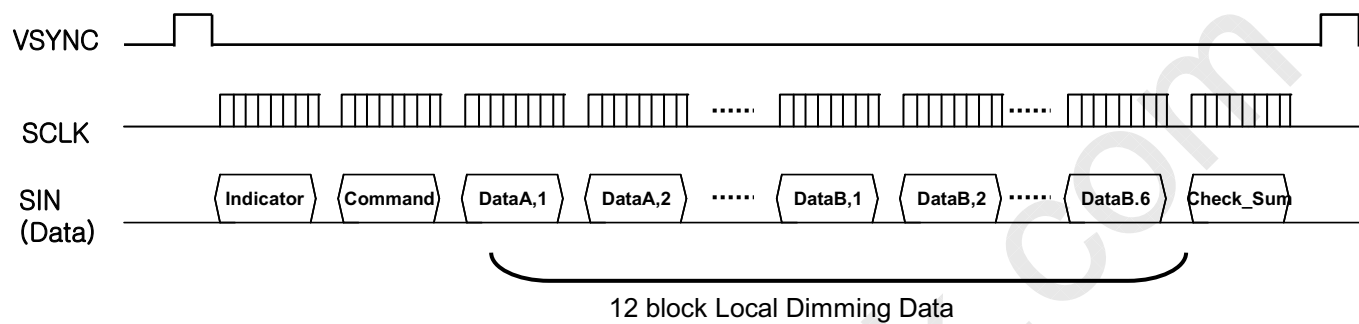
| Parameter<br>(DCLK rising edge기준) | Values |     |       | Units |
|-----------------------------------|--------|-----|-------|-------|
|                                   | Min    | Typ | Max   |       |
| T1                                | 6.00   | -   | 30.00 | us    |
| T2                                | 2.00   | -   | 10.00 | us    |
| T3                                | 1.00   | -   | 5.00  | us    |
| T4                                | 6.00   | -   | 30.00 | us    |
| T5                                | 20.00  | -   | 40.00 | us    |

LC420EUD

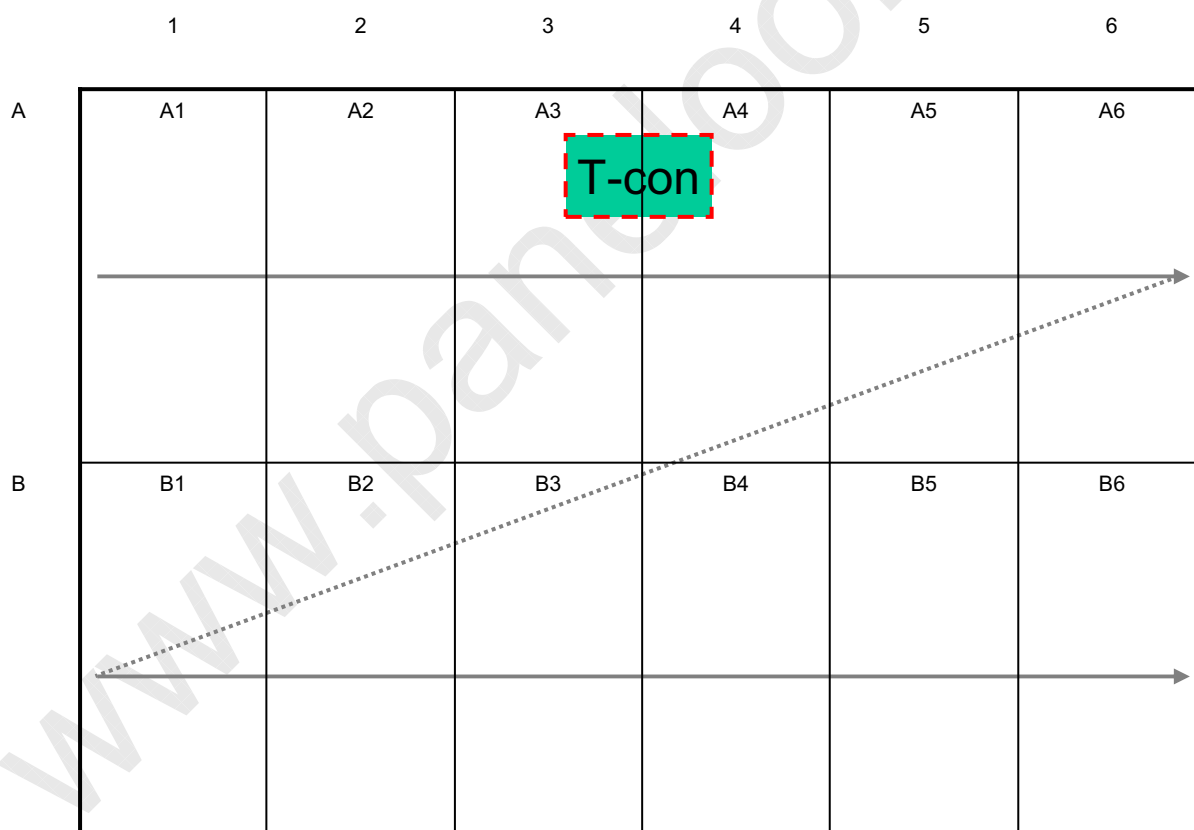
## Product Specification

## # APPENDIX- VI

## ► Local Dimming Block Mapping



## Front

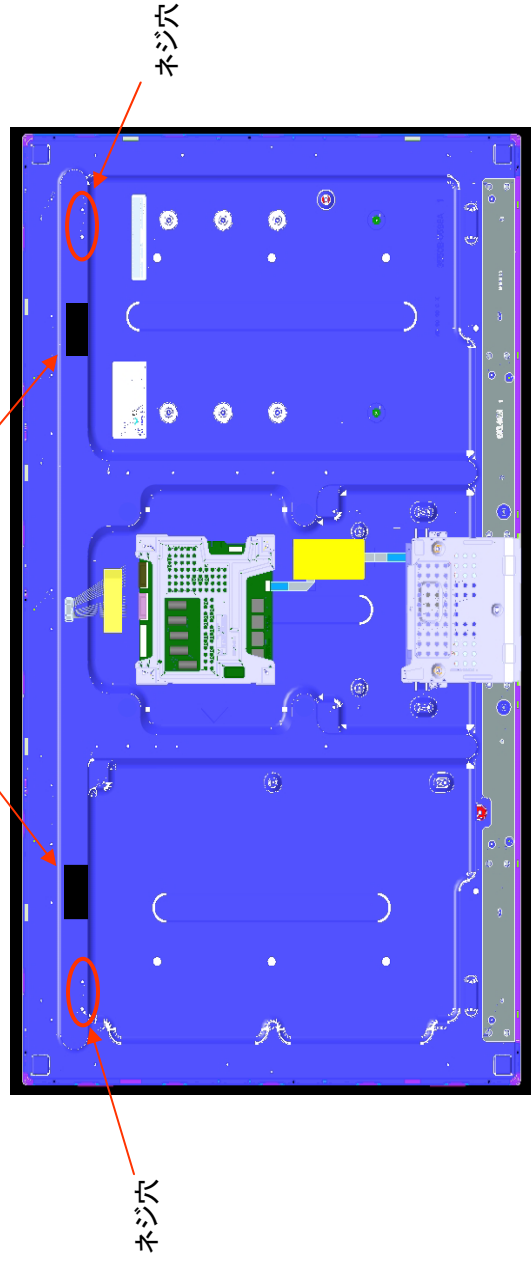


## LC420EUD-SCA2 背面光漏れTape附着位置



■ 光漏れ防止 Tape 附着位置

光漏れ防止 Tape



LC420EUD-SCA2

# Cube2 I2C slave mode

1. Power On Sequence

2. Built in I2C slave

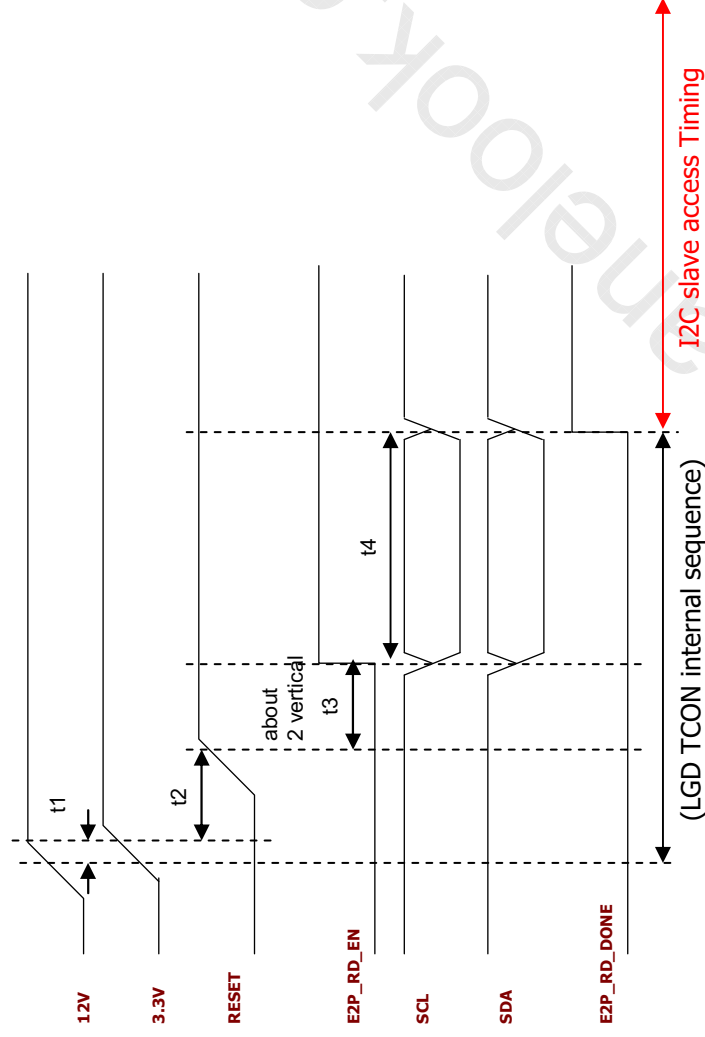


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2010. 03. 02.



# 1. Power On Sequence



- E2P\_RD\_EN : I2C Start signal. (LGD TCON internal signal)
- E2P\_RD\_DONE : I2C End signal TCON. (LGD TCON internal signal)

→ t1 + t2 : 20ms

→ t3 + t4 : 230ms (based on worst case)

→ **T-Con 12V slave access 可能時間: 12V power 認可後 t1 + t2 + t3 + t4 時間以降(250ms)**

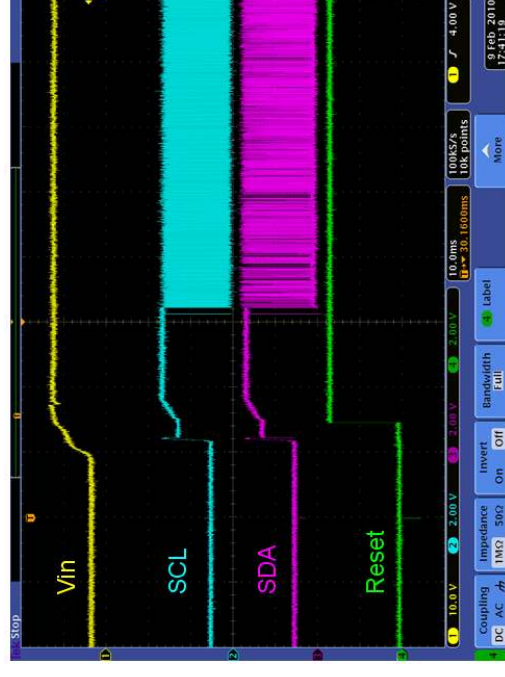
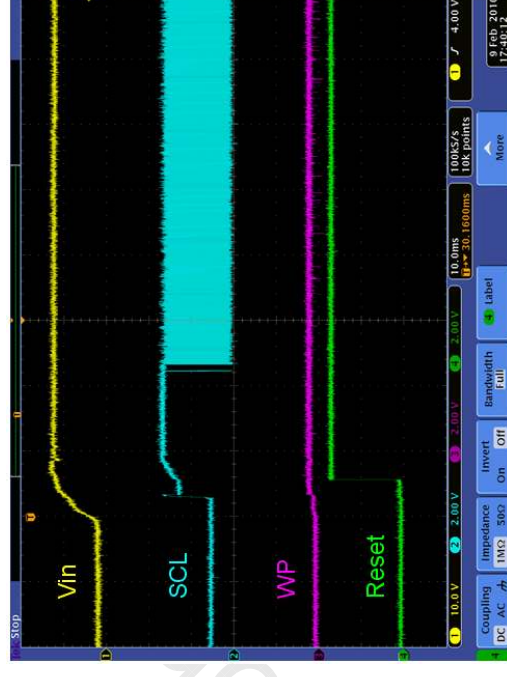


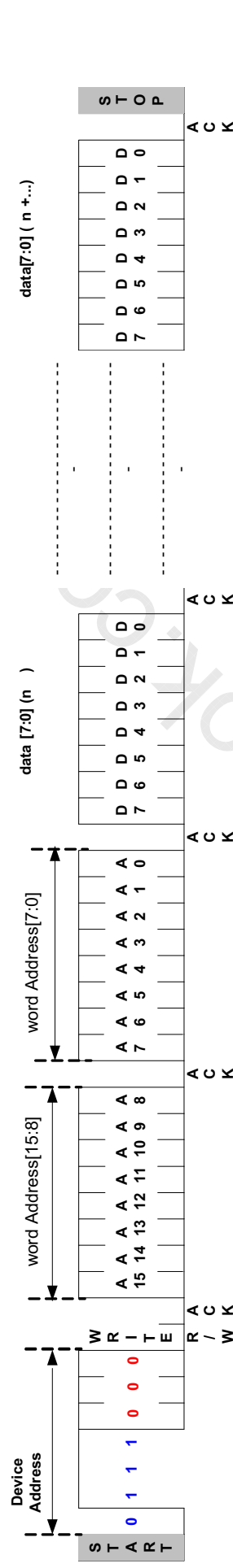
Figure1. V5 Edge T-con power sequence



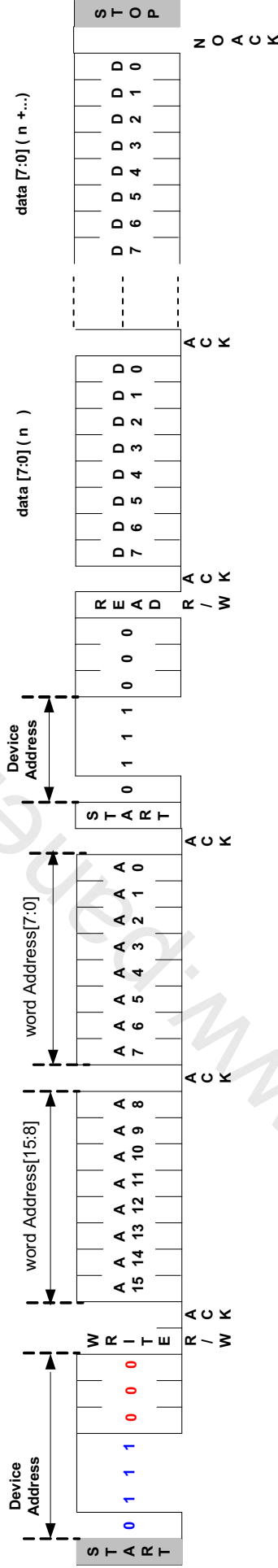
## 2. Built in I2C slave

- T-Conで I2C slaveに accessする場合下記の protocol 方式(burst mode)で accessする。

### ■ Register write



### ■ Register read



**\*DGA functionと関連された register 部分(address hx230 ~ hx6AF 該当)は必ず access する。**

ex) 該当 addressで accessするのは間違いである。

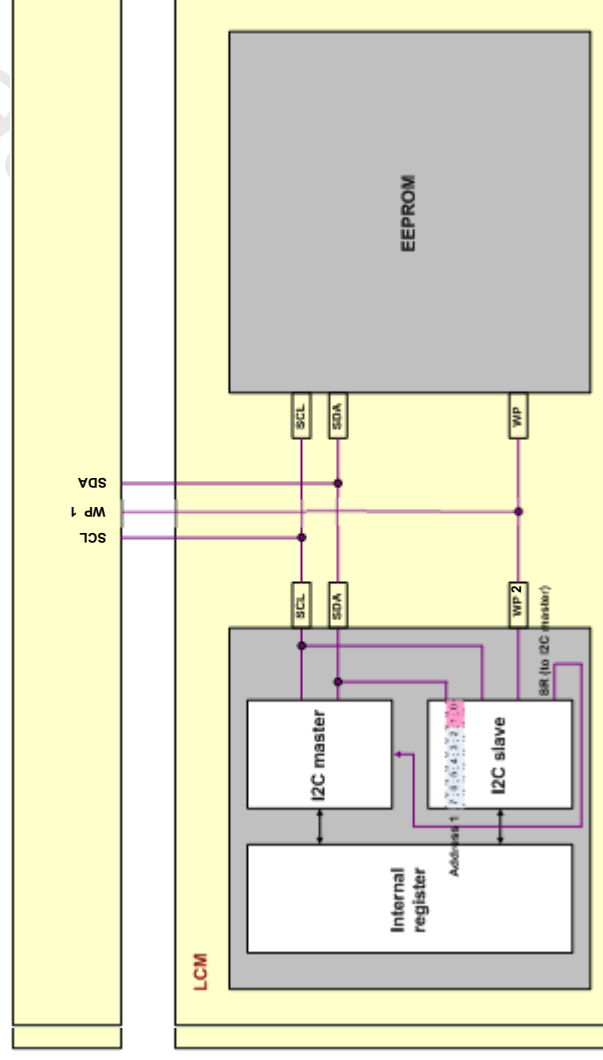
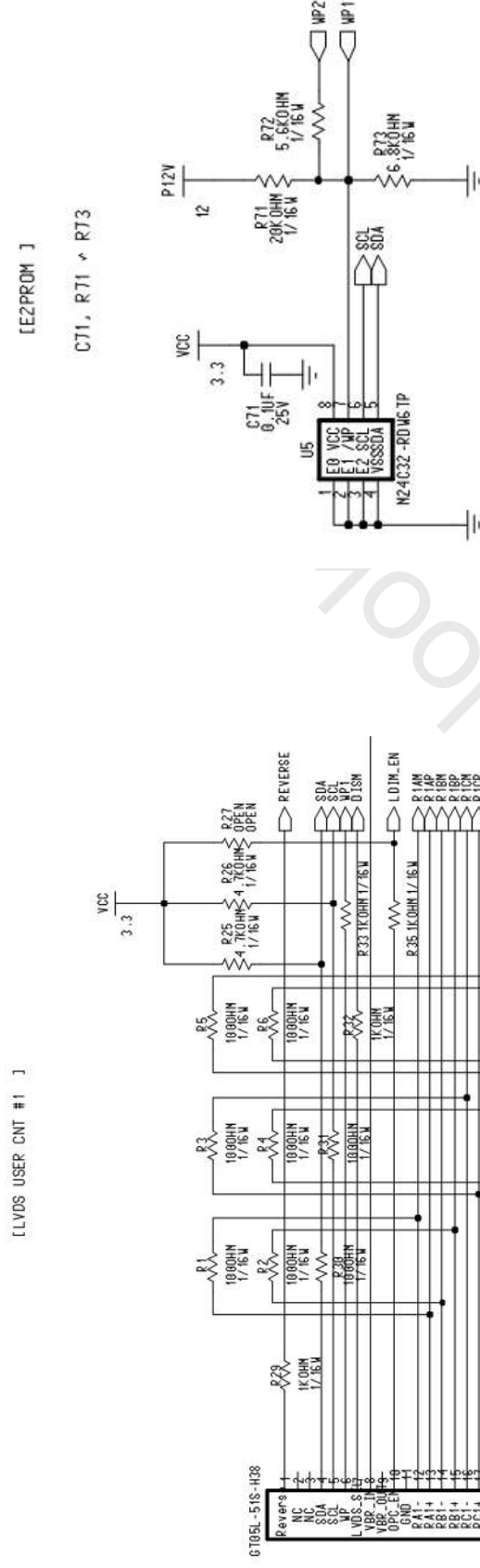
hx230 ~ hx600 → X, hx100 ~ hx400 → X, hx300 ~ hx6AF → X

該当 addressで accessするのは DGA functionと関連された registerを一度にaccess出来る範囲なので OK

hx230 ~ hx6AF → O, hx230 ~ hx6C2 → O, hx100 ~ hx6FF → O



### 3. PCB schematic I2C Bus







이재우

김민준

이재우

# I2C standard spec

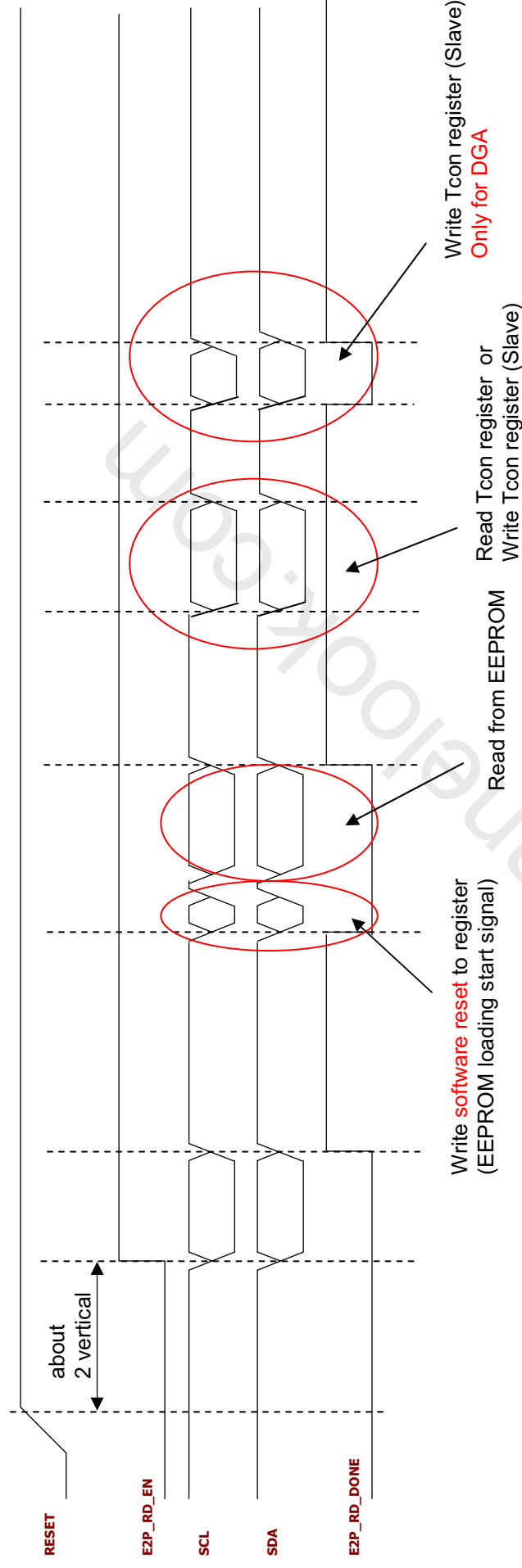
1. Power On Sequence
2. EEPROM Access Flow Chart
3. Built in I2C slave

2009. 12. 15

T-Con 開発チーム



# 1. Power On Sequence



■ E2P\_RD\_DONE : I2C End signal TCON (I2C module → TCON module).

1) Power on : It transits (Low--> High) only after loading EEPROM data.

2) Software Reset : -. High --> Low

: It transits the point of start

- Low --> High

: It transits the point of EEPROM loading end

3) Slave for DGA : -. High → Low

: It transits the point of hx230, when slave processing.

-. Low → High

: It transits the point of hx6AF, when slave processing.

4) The other cases : It stays " High"

Cf.) "3)" 項目のような場合には address access を必ず hx230 ~ hx6AF で一度に access することで user と協議されている事項である。

ex) hx230 ~ hx600 → X

hx100 ~ hx400 → X

hx300 ~ hx6AF → X

hx230 ~ hx6AF → O

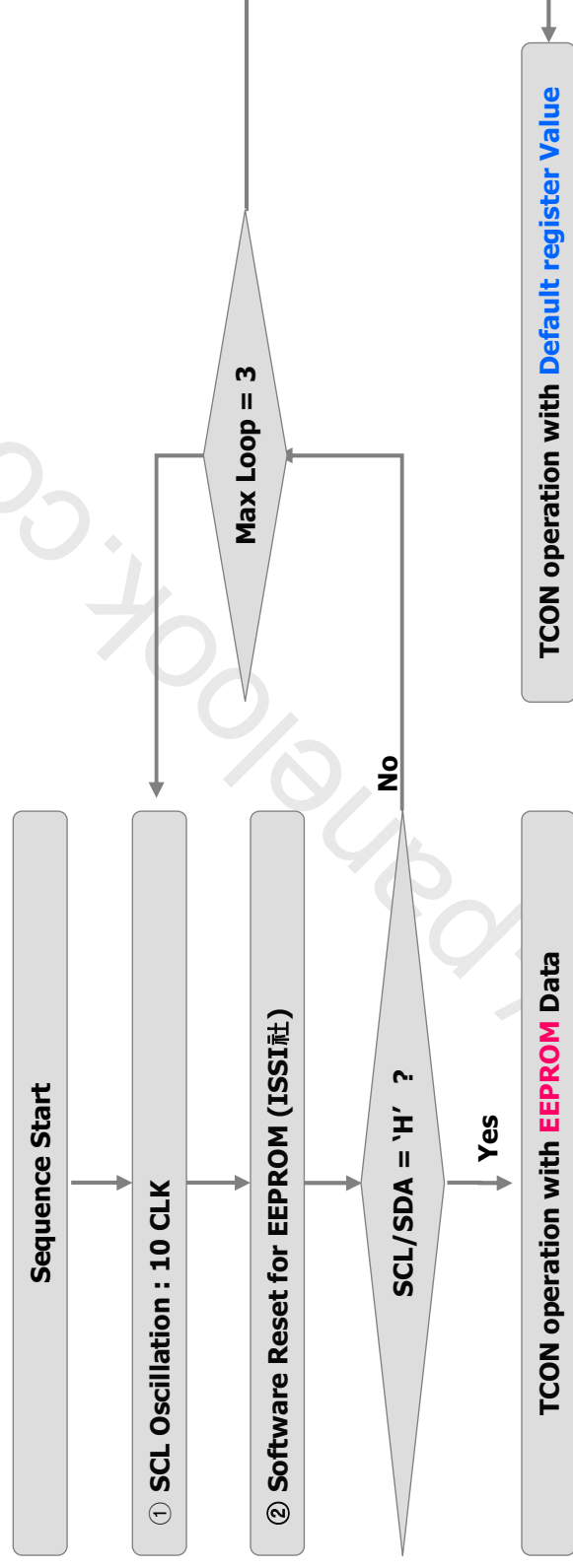
hx230 ~ hx6C2 → O

hx100 ~ hx6FF → O



## 2. EEPROM Access Flow Chart

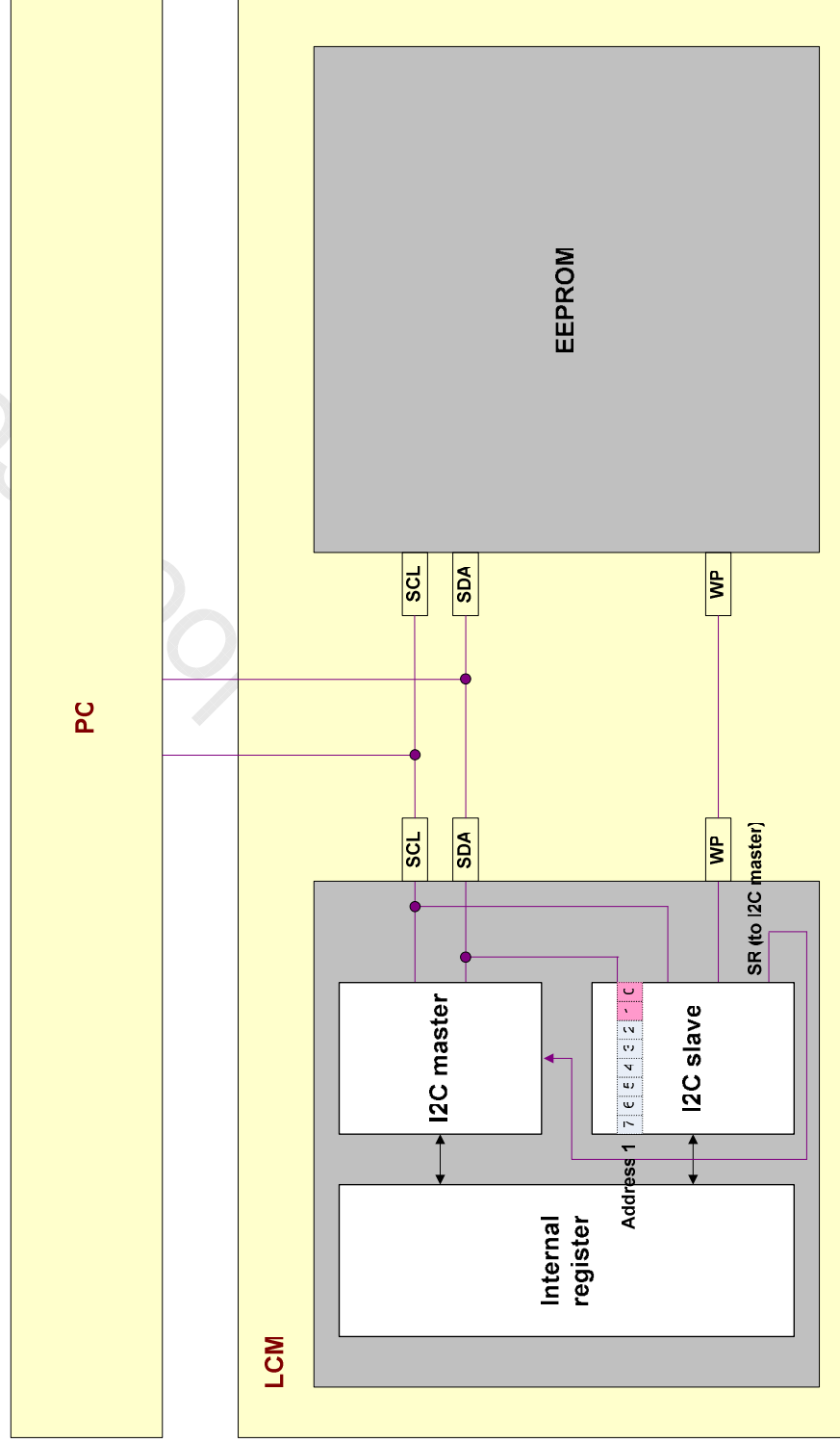
- SCL & SDAがHighの場合は EEPROM Data を Load して使うように構成された TCONにおいて、EEPROMの malfunction を防止するためには今後の標準に選定される TCON の I2C access の flow chart を図に表す。  
ただ、外部 I2C\_EN pin があるって EEPROM Data を使うように setting する場合、①→②→Load の sequence を 1 回遂行する。





### 3. Built in I2C slave

- Slave によって TCON の Register を control できるようにする。  
TCON に I2C slave を内蔵して SR (EEPROM's software reset と違う) と EEPROM の WP を Control できるようにする。  
SR( FF 番地の 1 番 bit) と WP(FF 番地の 0 番 bit : LSB) を制御することができる **address は FF 番地** を使うようにする。
- \* **co-worker 注意事項** : EEPROM FF 番地に保存された data が TCON 動作に影響を与えないようにする。(I2C Master によって動作しない。)
- \* **LGD 注意事項** : EEPROM's FF 番地には TCON 動作と係わった register を使わないようにする。

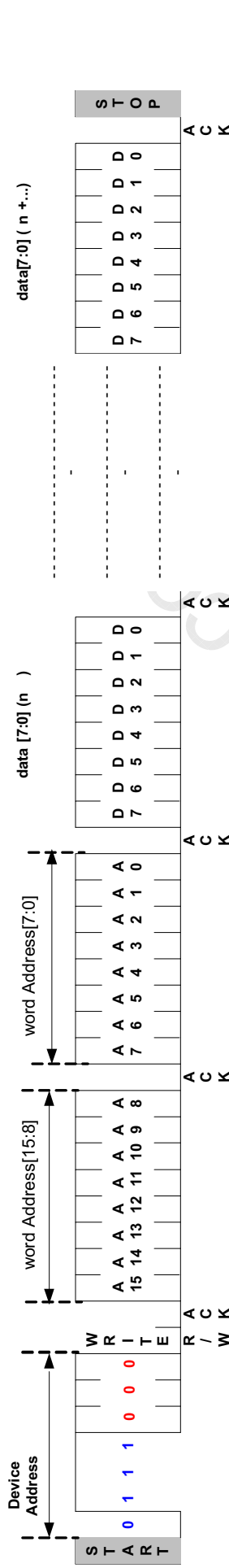


\* SR : Software Reset , WP : Write Protect

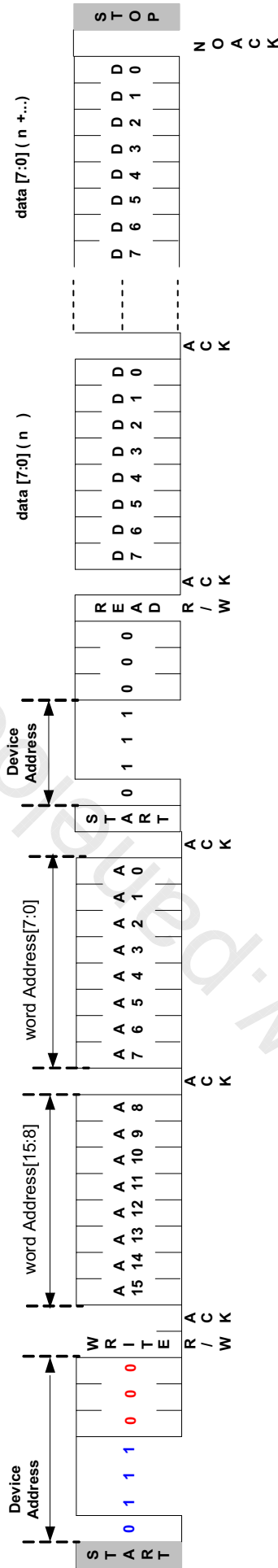


### 3. Built in I2C slave

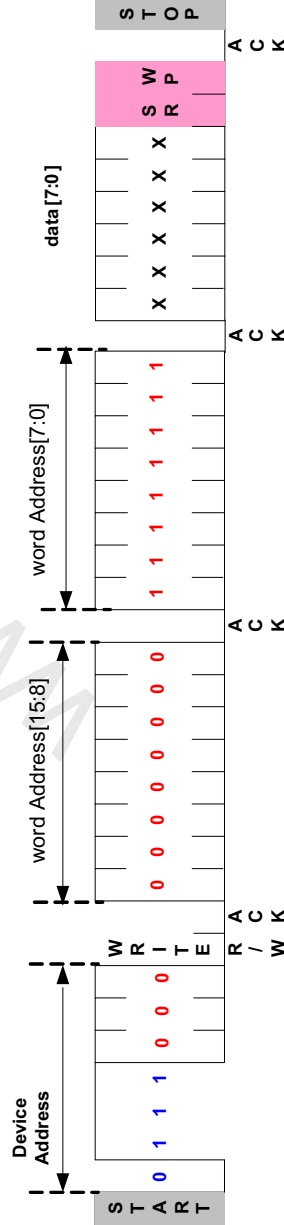
#### Register write



#### Register read



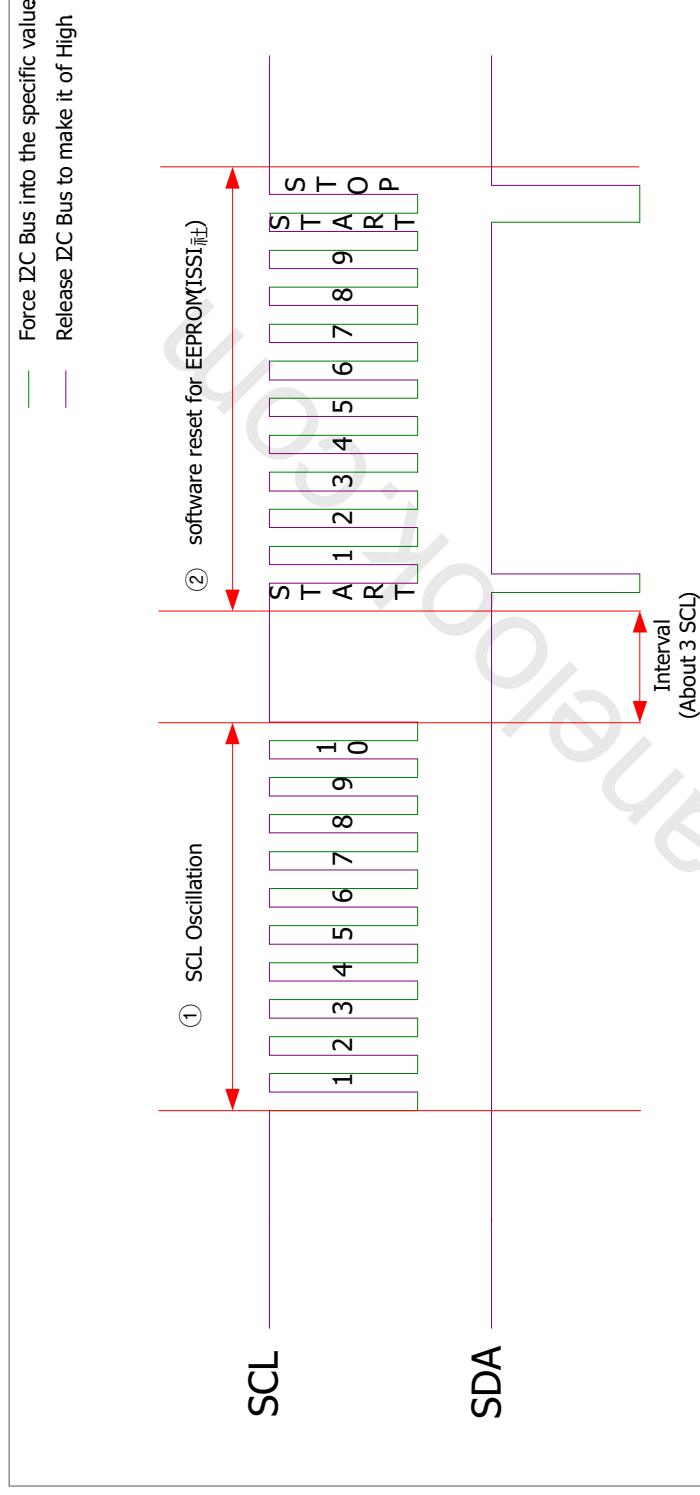
#### Control Software Reset & Write Protect



WP : 1 → LOW \*初期 Power On 及び SRによって Restart の時  
0 → HIGH 出力ピンの WP で High が出力されるようにする。  
SR : 1 → Software Reset Enable  
0 → Software Reset Disable



## Ref 1. Waveform of initializing EEPROM



| No | Item                      | 提案会社           | 解決可能不良現象                                                    | remark                                                                                                                 |
|----|---------------------------|----------------|-------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------|
| 1  | SCL Oscillation           | -              | - EEPROM force SDA into Low                                 | - ST4_HF 検討 中, malfunction 防止のための Conceptが不在.<br>- SW社が防止策を用意するために実験的に導出した Concept.                                    |
| 2  | software reset for EEPROM | ISSI社<br>ROHM社 | - EEPROM force SDA into Low<br>- EEPROM send the wrong data | - EEPROM malfunctionのために software reset 使用勧告。<br>- ROHM社が ISSI社の software resetをサポート可能だと言って ISSI社 softwareを標準で行くことに決定。 |